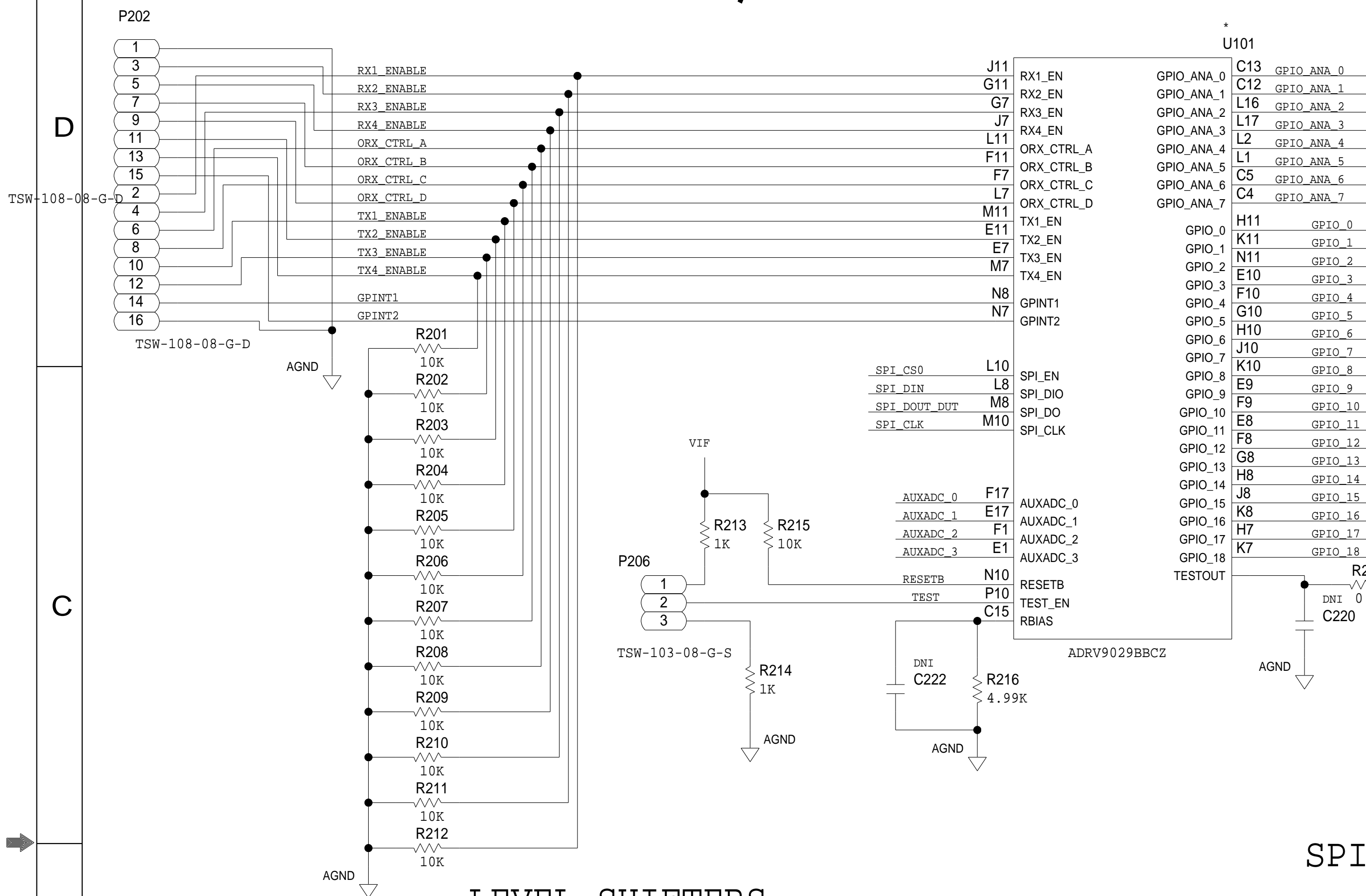
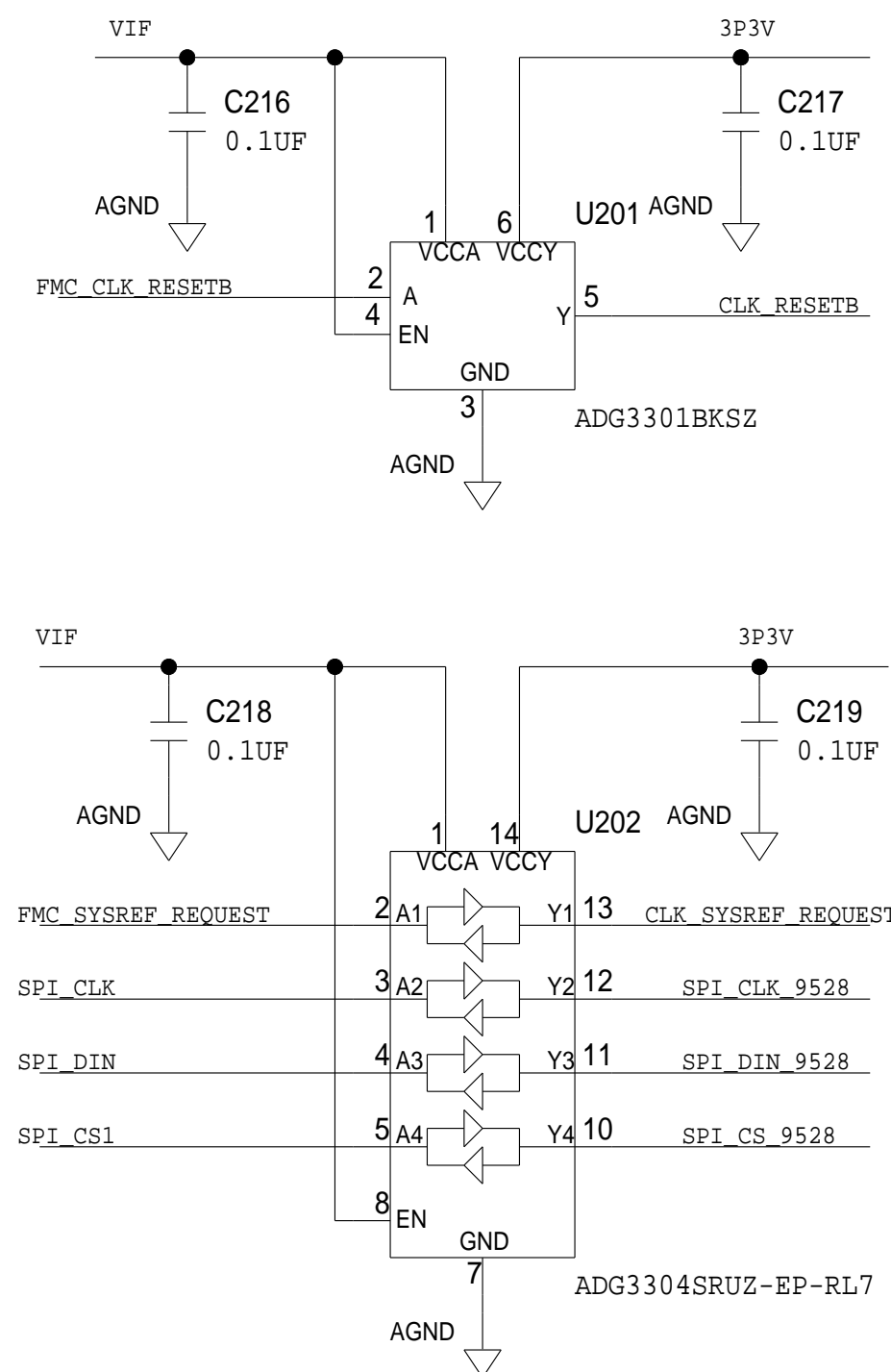


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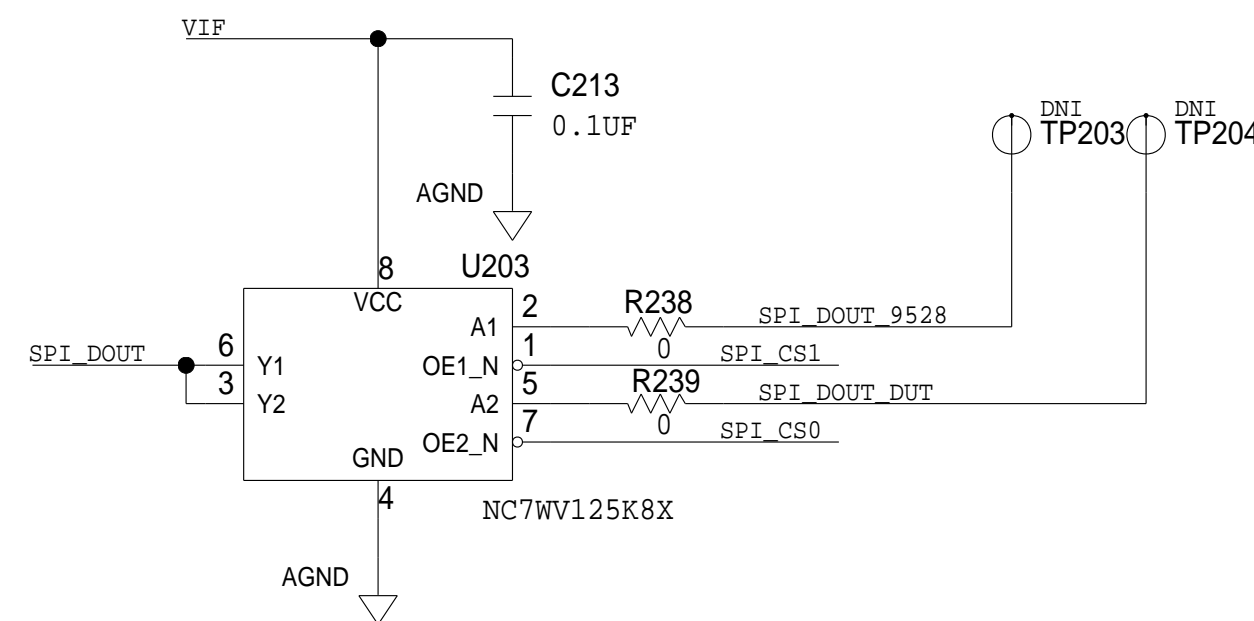
DIGITAL I/O



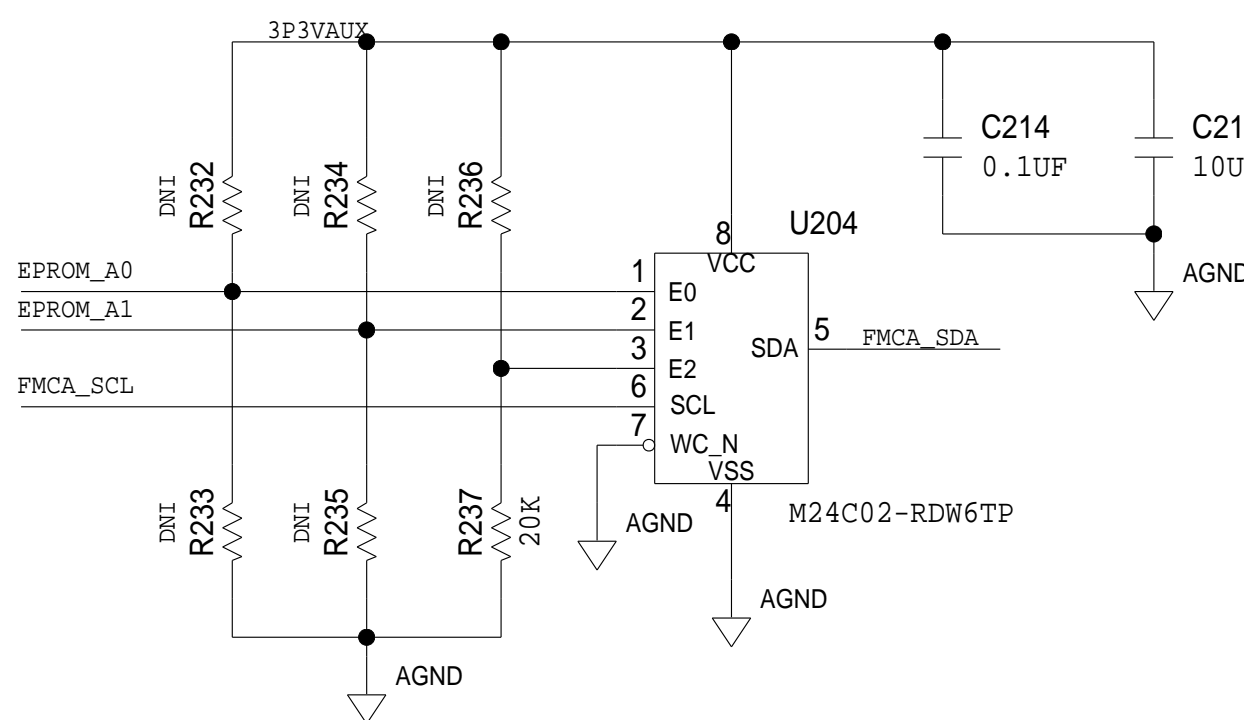
LEVEL SHIFTERS



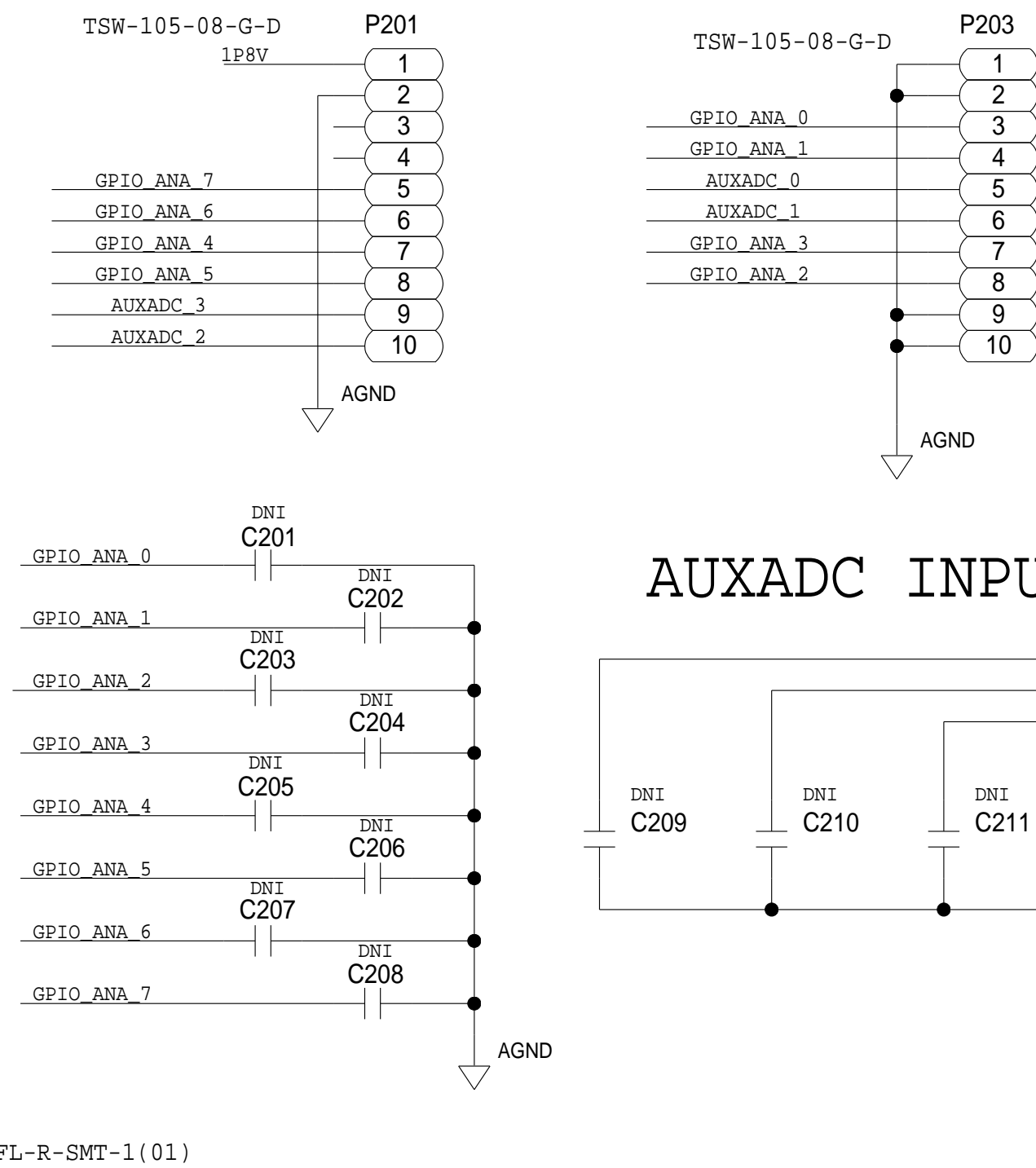
SPI INTERFACE BUFFER



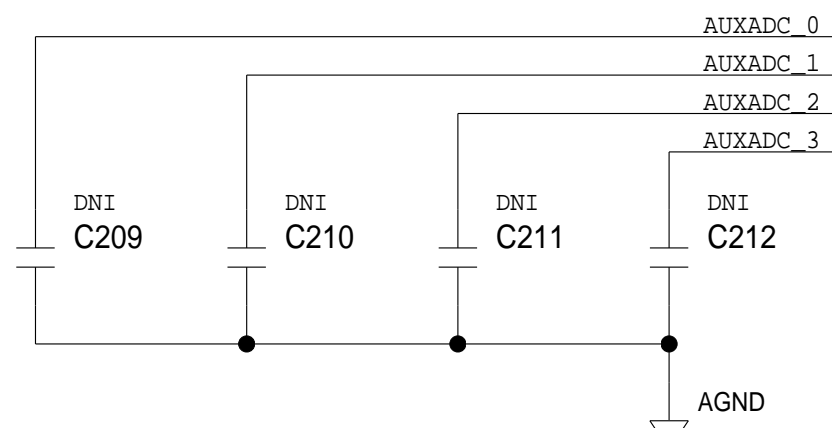
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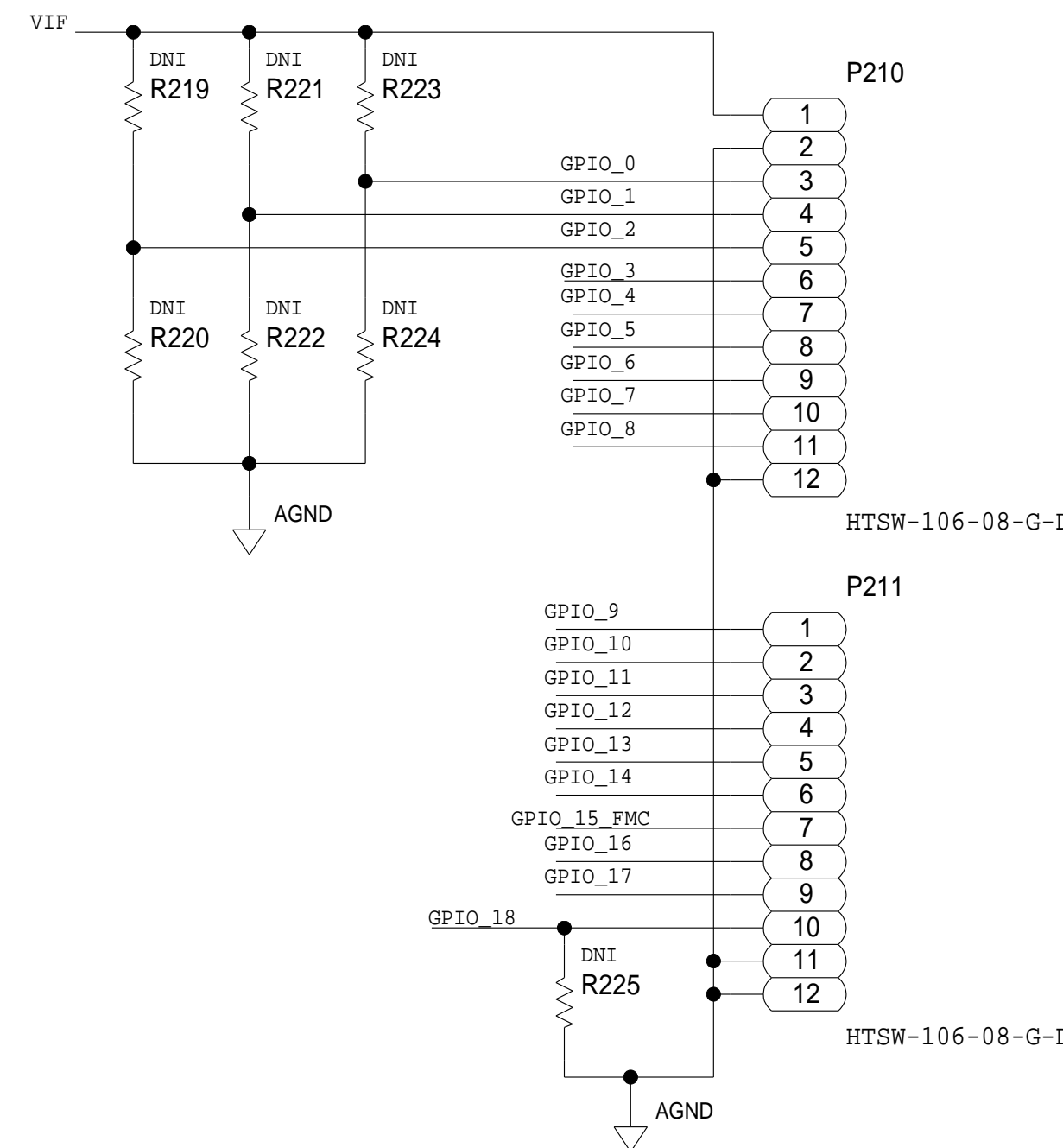
AUXDAC OUTPUTS AND 1.8V GPIO



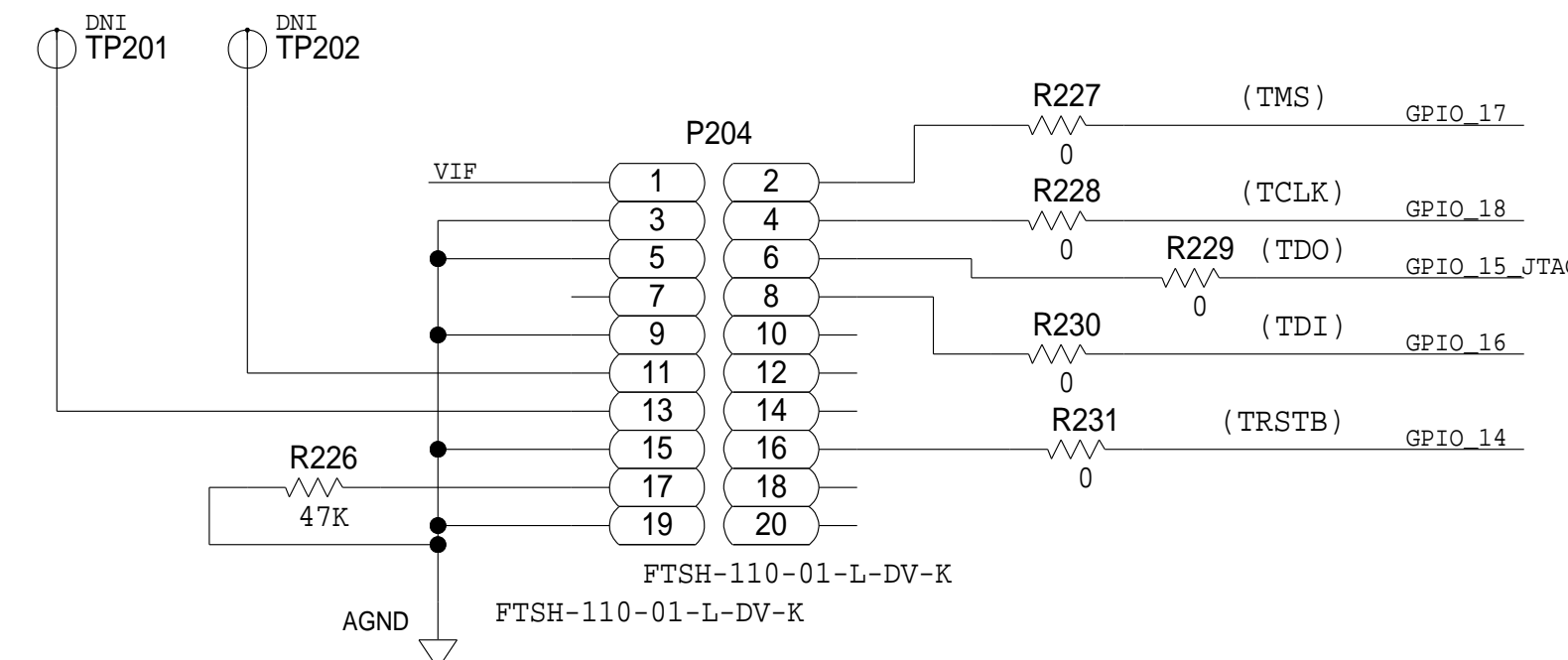
AUXADC INPUTS



DIGITAL GPIO



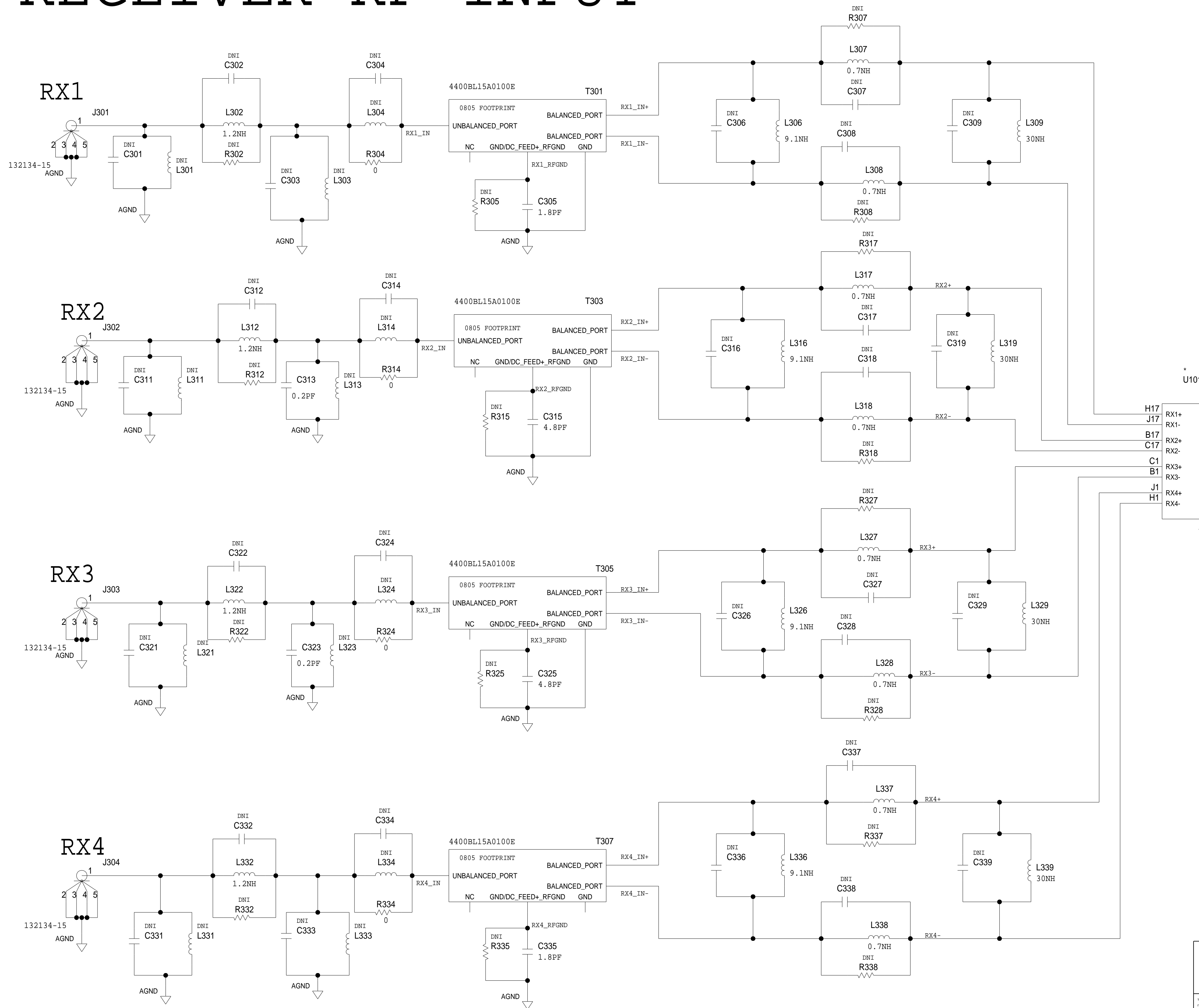
JTAG INTERFACE



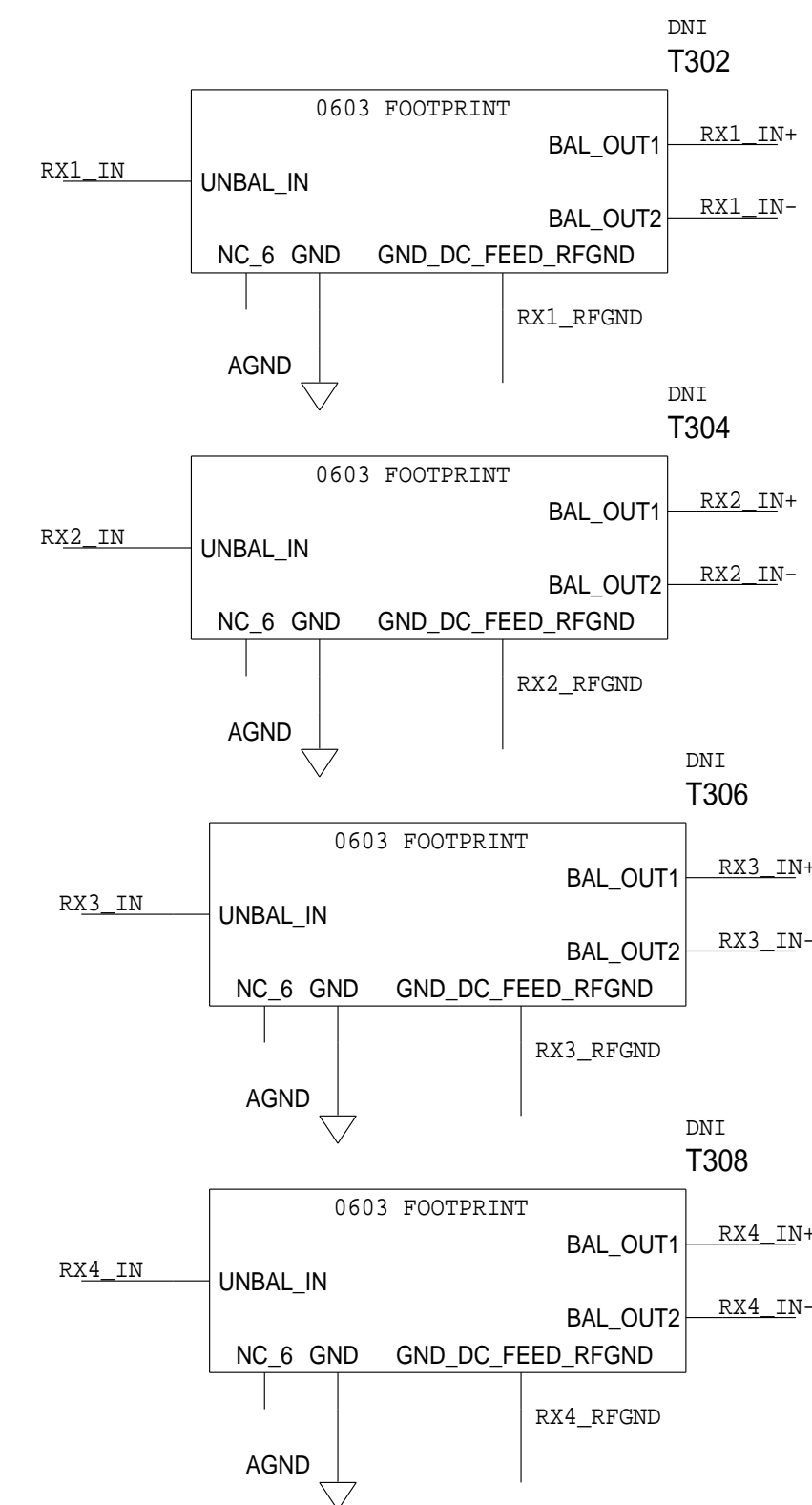
REVISIONS				
REV	DESCRIPTION	DATE	APPROVED	

ANALOG DEVICES		SCHEMATIC		
HW TYPE : Customer Evaluation Product(s) : ADRV9025, ADRV9026, ADRV9029 : ADRV9029-HB		DESIGN VIEW : ADRV9029-HB		REV C
PTD ENGINEER R. MYERS		DRAWING NO. 02_049168-05	SIZE D	SCALE 1:1
		SHEET 2	OF 10	

RECEIVER RF INPUT



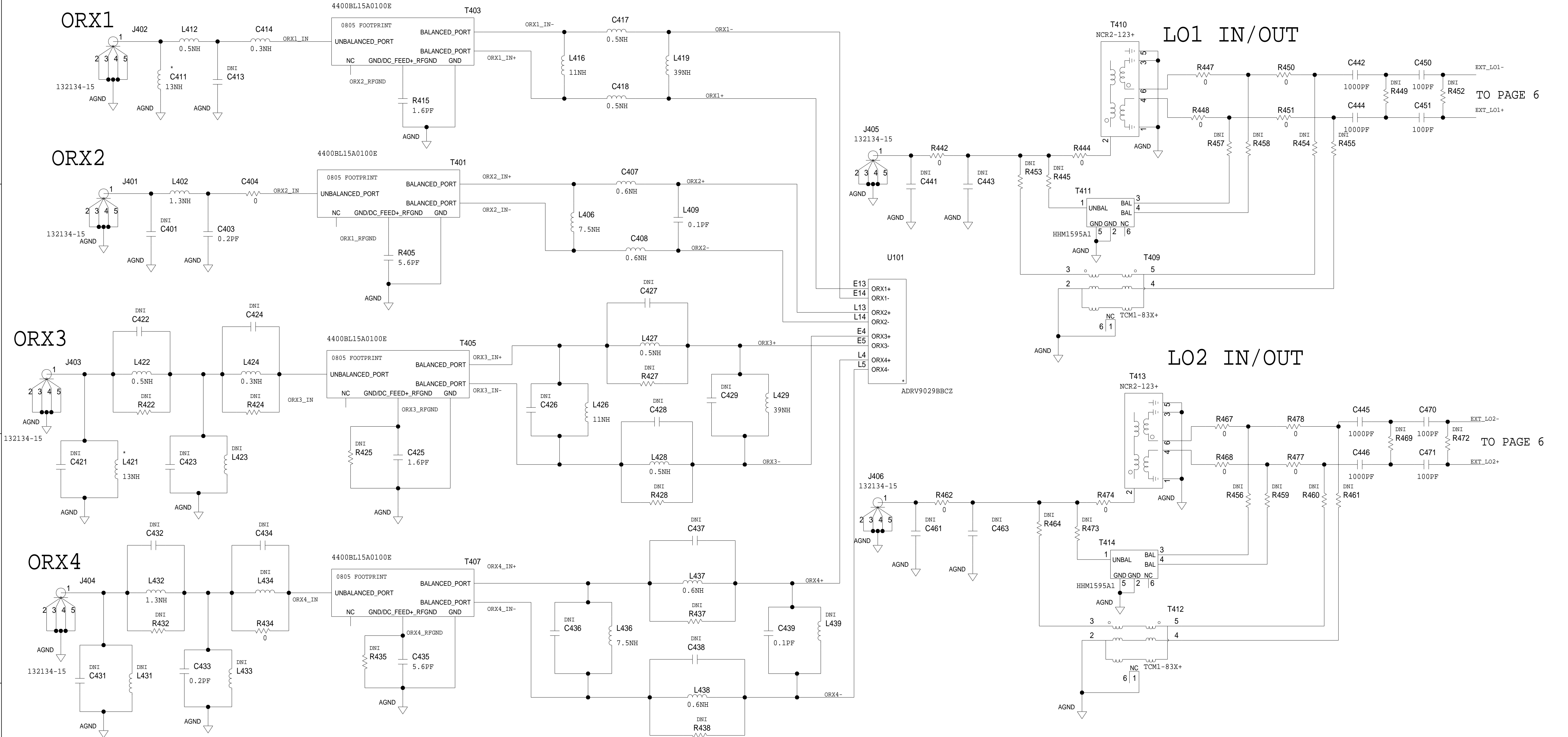
OVERLAP RX BALUNS



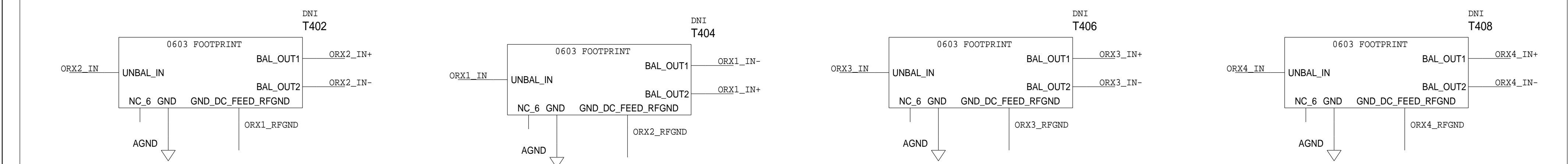
SCHEMATIC				
HW TYPE : Customer Evaluation Product (s) : ADRV9025,ADRV9026,ADRV9029 : ADRV9029-HB				
DESIGN VIEW : ADRV9029-HB	DRAWING NO. 02_049168-05	REV C		
PTD ENGINEER R. MYERS	SIZE D	SCALE 1:1	SHEET 3	OF 10

OBSERVATION RX RF INPUT

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



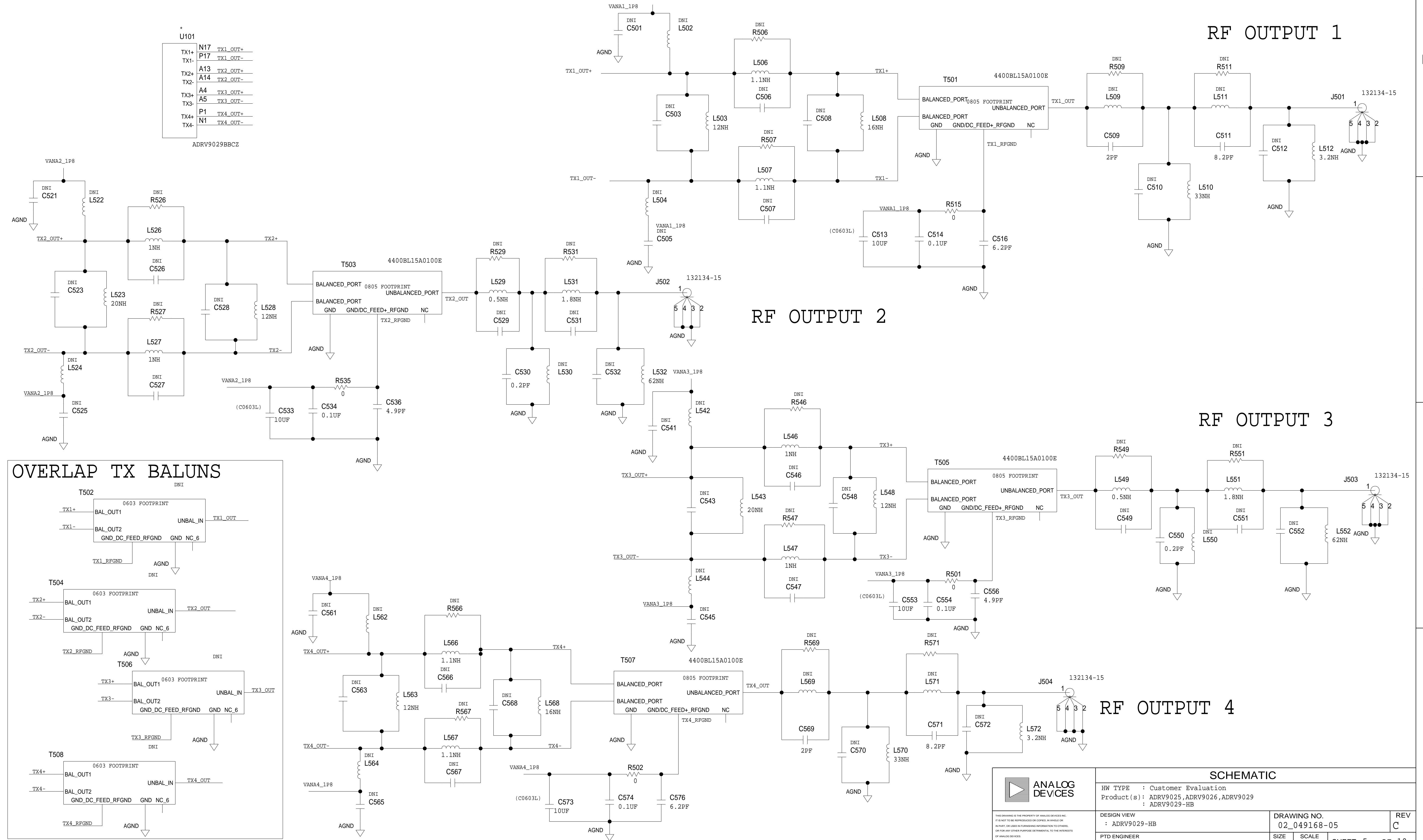
OVERLAP ORX BALUNS

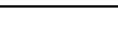


SCHEMATIC			
HW TYPE : Customer Evaluation Product(s) : ADRV9025,ADRV9026,ADRV9029 : ADRV9029-HB			
DESIGN VIEW : ADRV9029-HB	DRAWING NO. 02_049168-05	REV C	
PTD ENGINEER R. MYERS	SIZE D	SCALE 1:1	SHEET 4 OF 10

TRANSMITTER RF OUTPUT

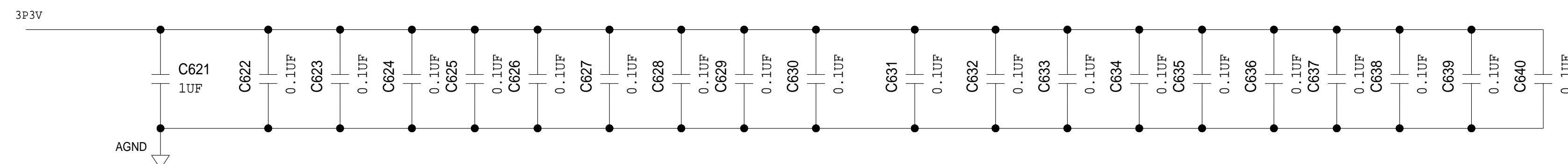
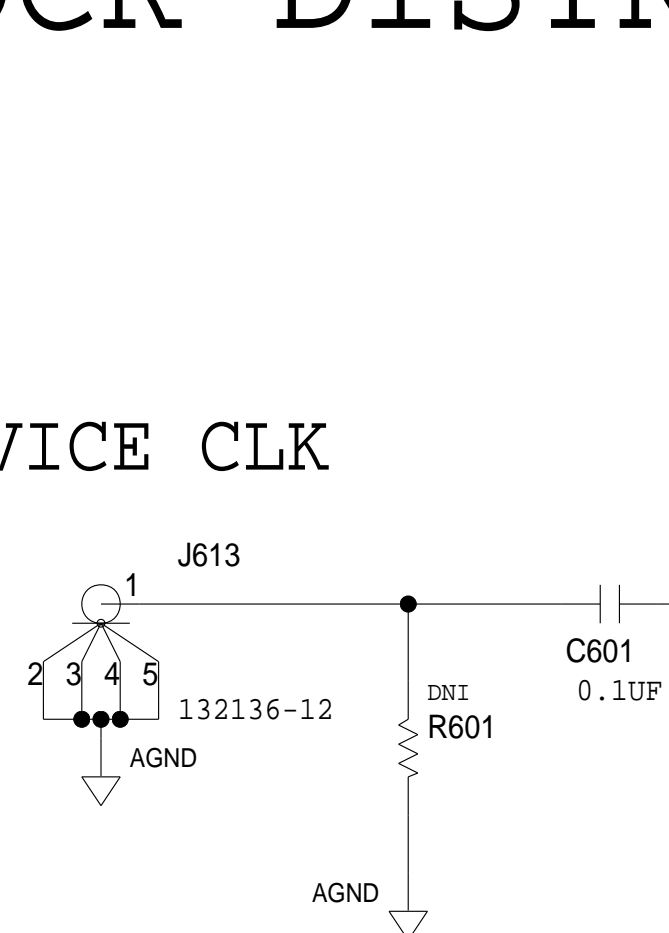
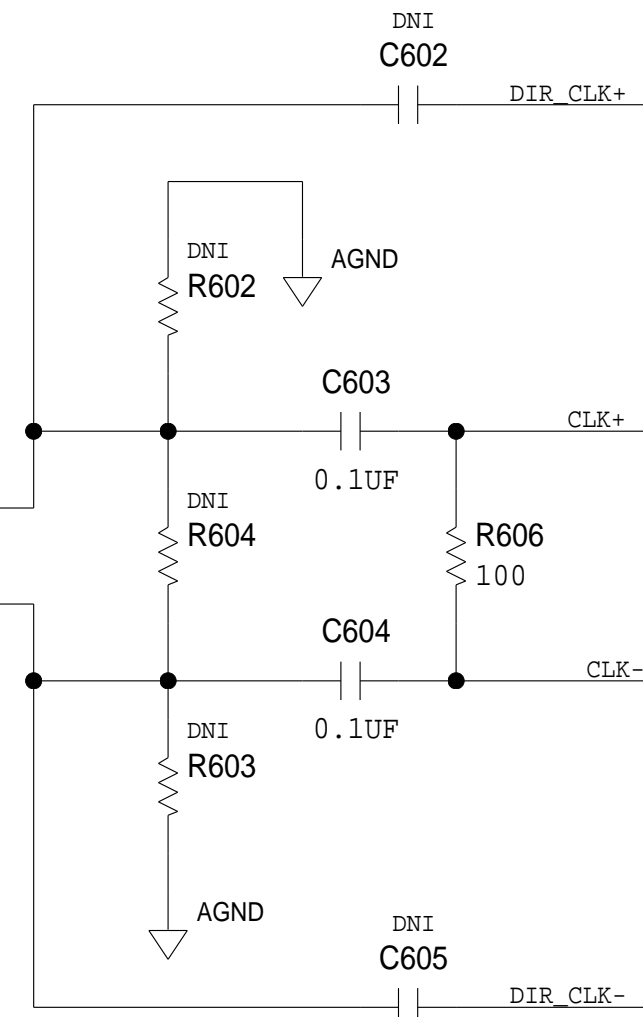
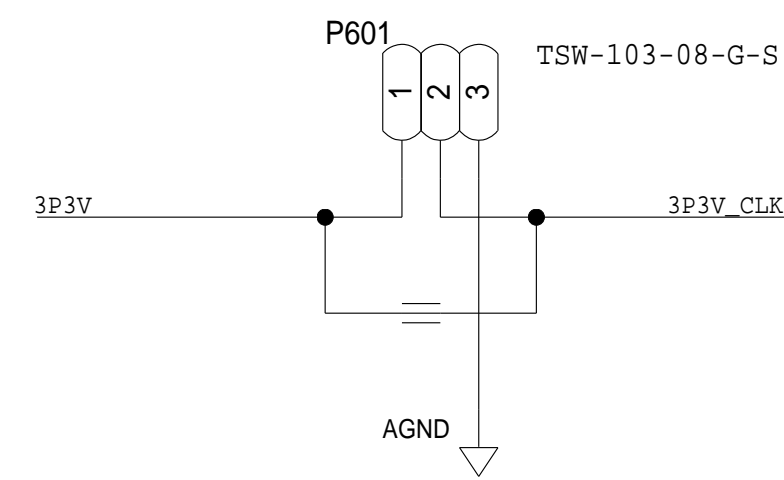
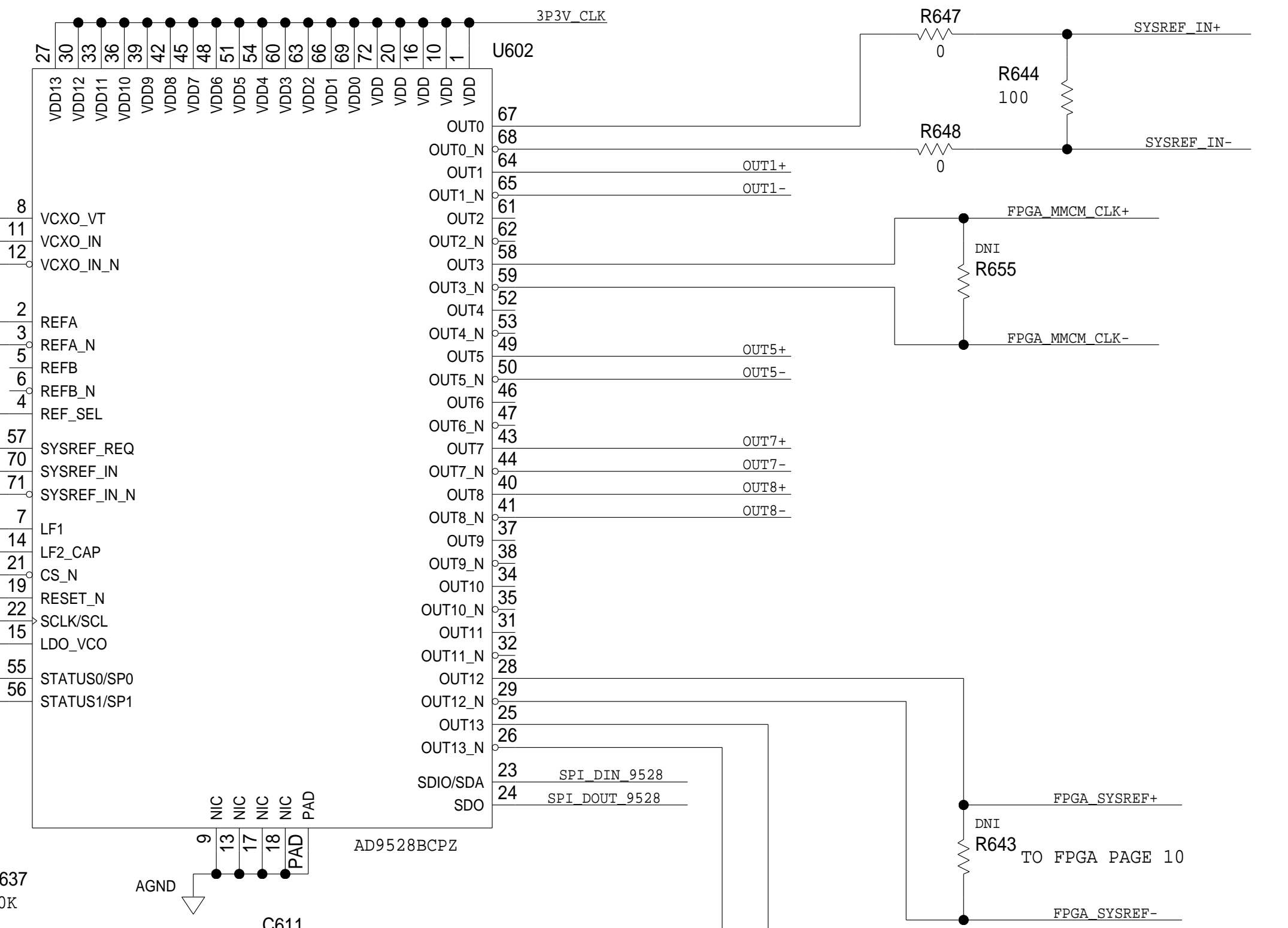
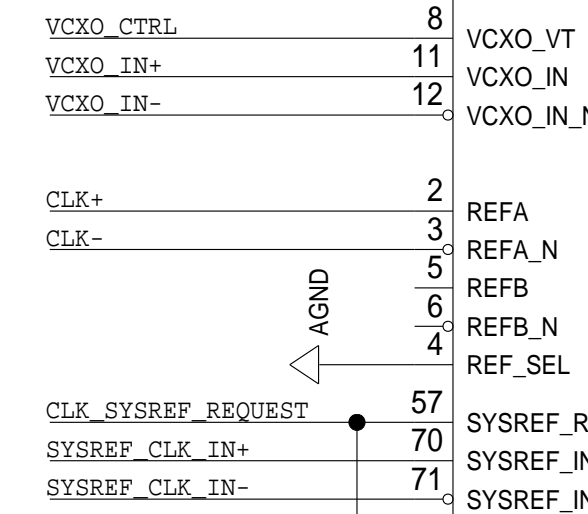
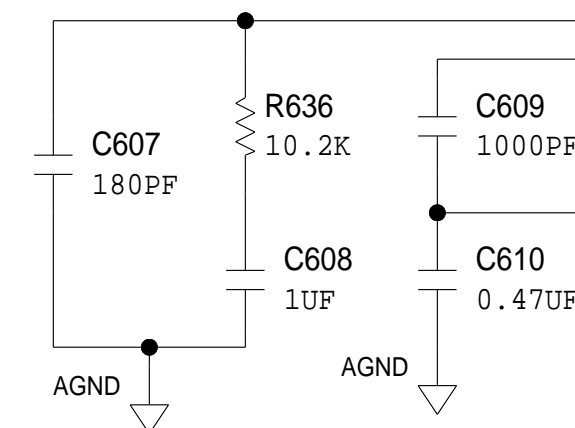
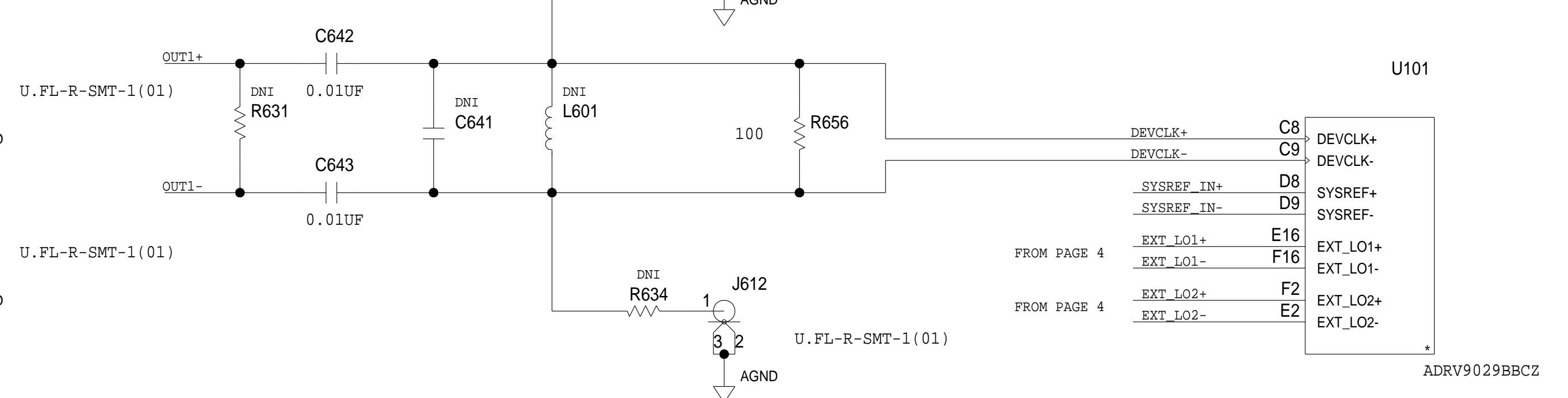
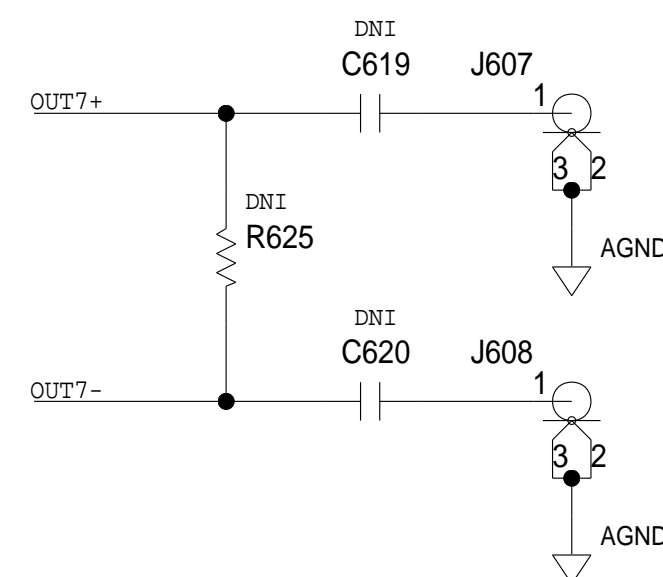
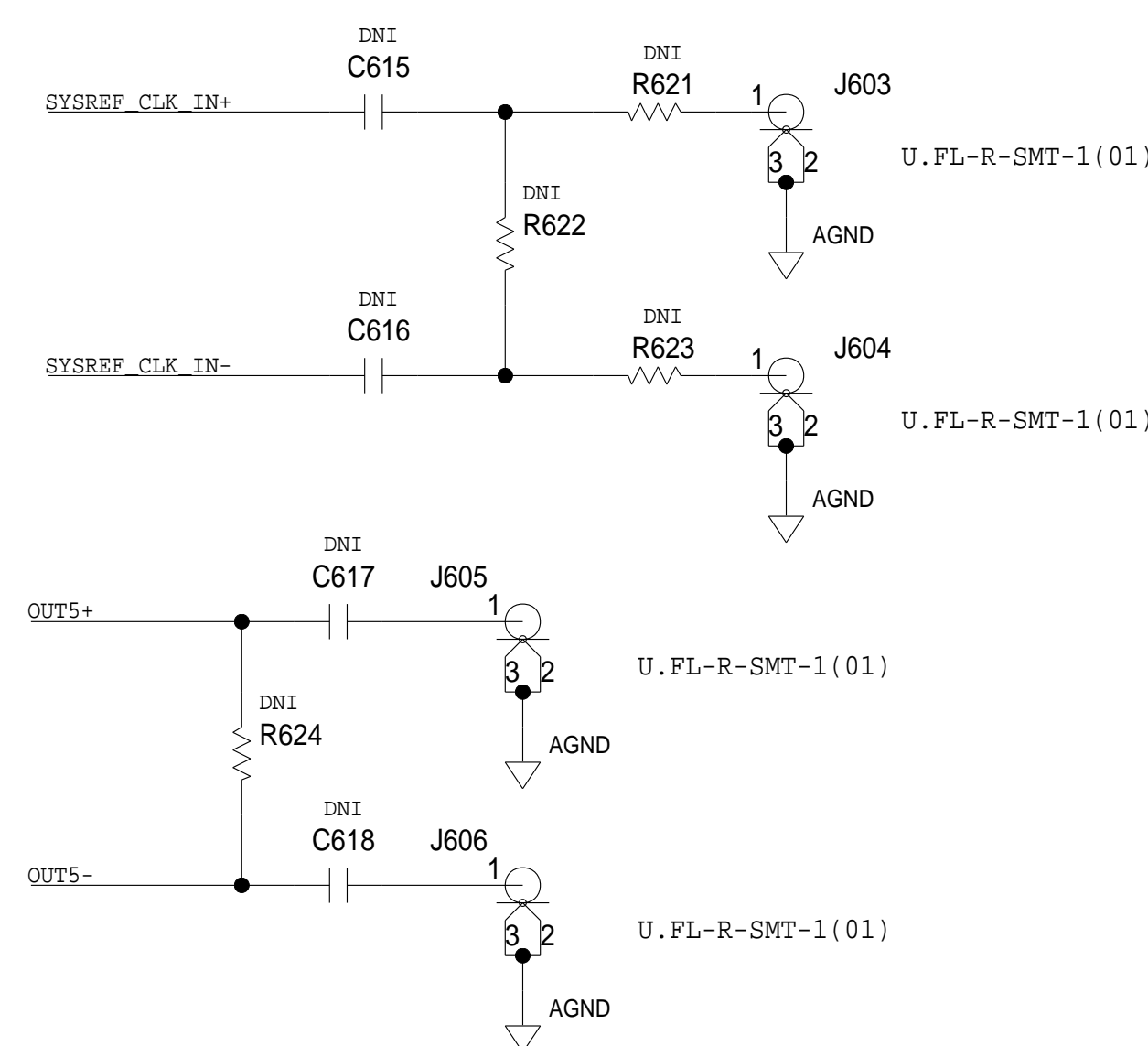
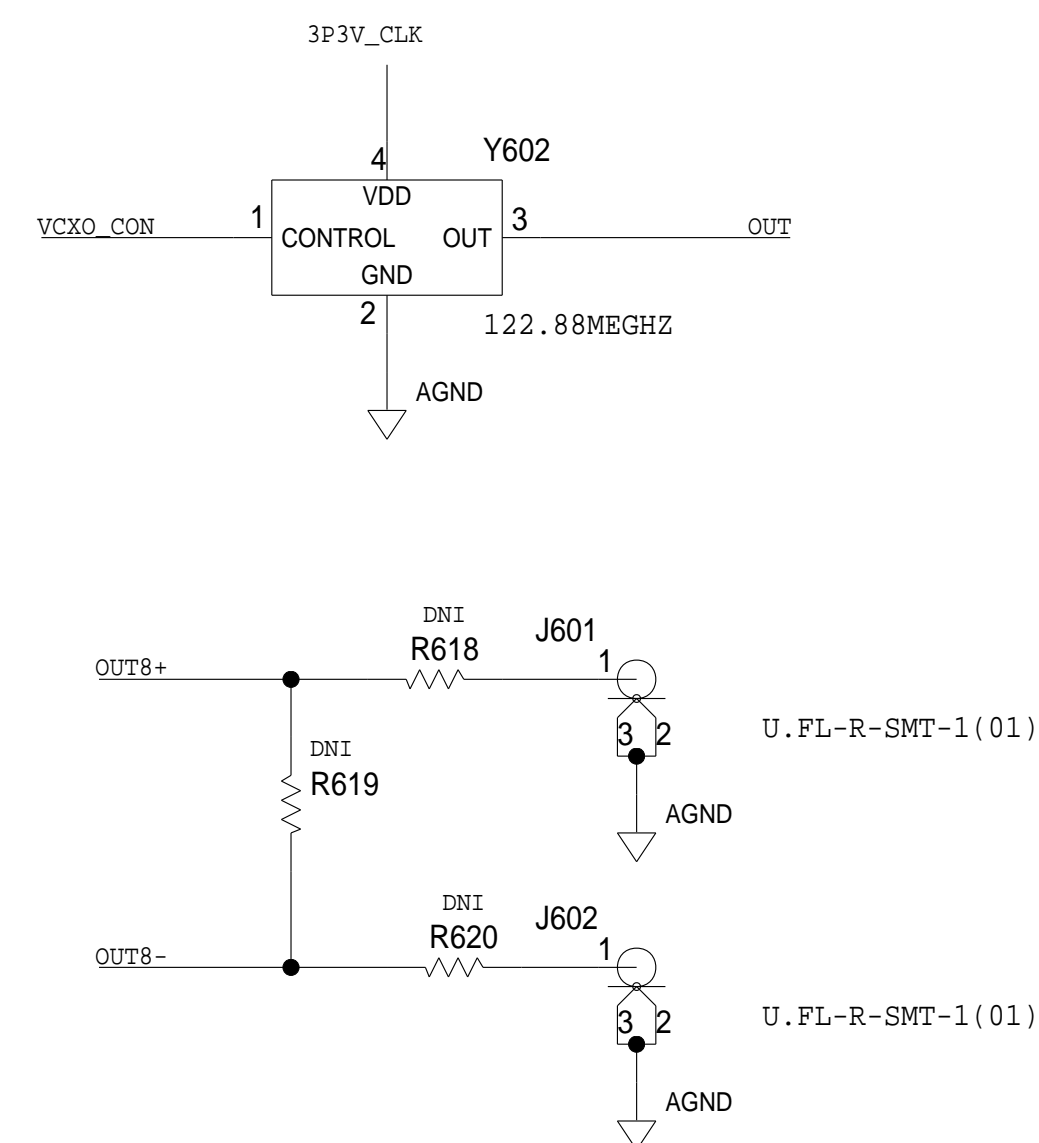
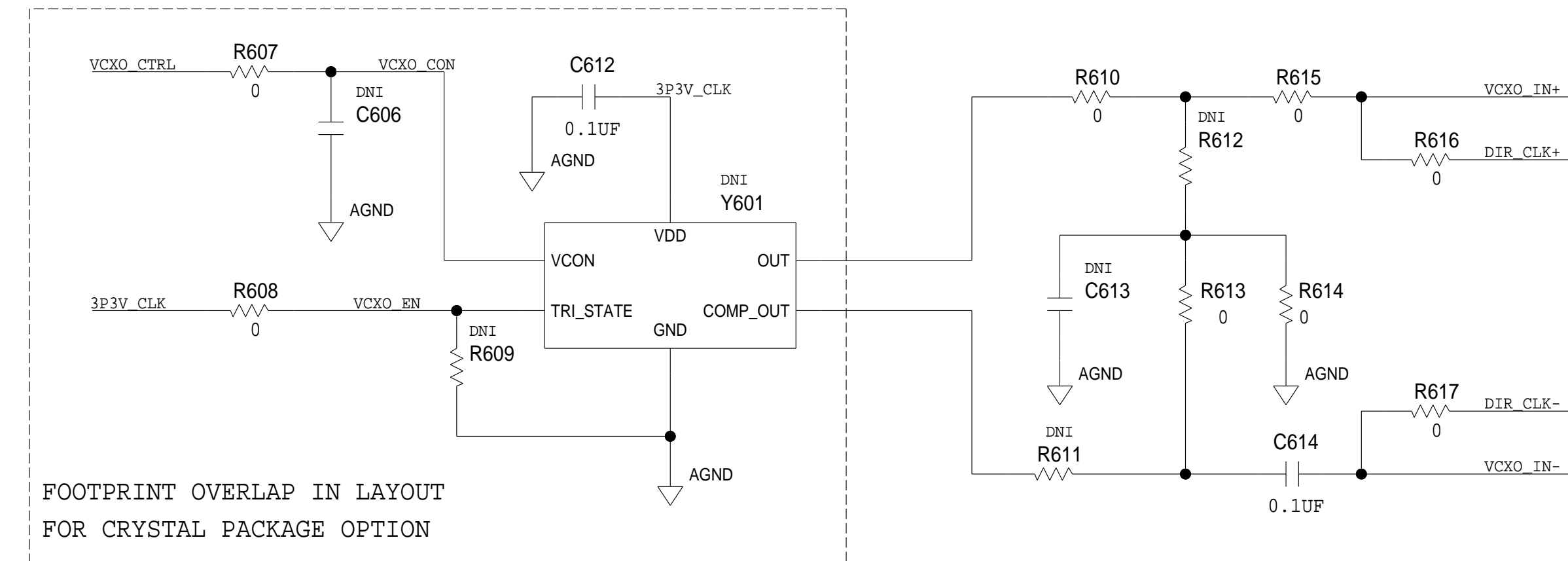
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED




 ANALOG DEVICES	SCHEMATIC			
	HW TYPE : Customer Evaluation Product(s) : ADRV9025,ADRV9026,ADRV9029 : ADRV9029-HB			
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	PTD ENGINEER R. MYERS	SIZE D	SCALE 1:1	SHEET 5 OF 10

CLOCK DISTRIBUTION

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



 ANALOG DEVICES	<h1 style="text-align: center;">SCHEMATIC</h1>			
	<p>HW TYPE : Customer Evaluation</p> <p>Product(s) : ADRV9025, ADRV9026, ADRV9029</p> <p>: ADRV9029-HB</p>			
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	PTD ENGINEER R. MYERS	SIZE D	SCALE 1:1	SHEET 6 OF 10

[illegible]

2
1

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

ADRV9029BBCZ

C789

0.1UF

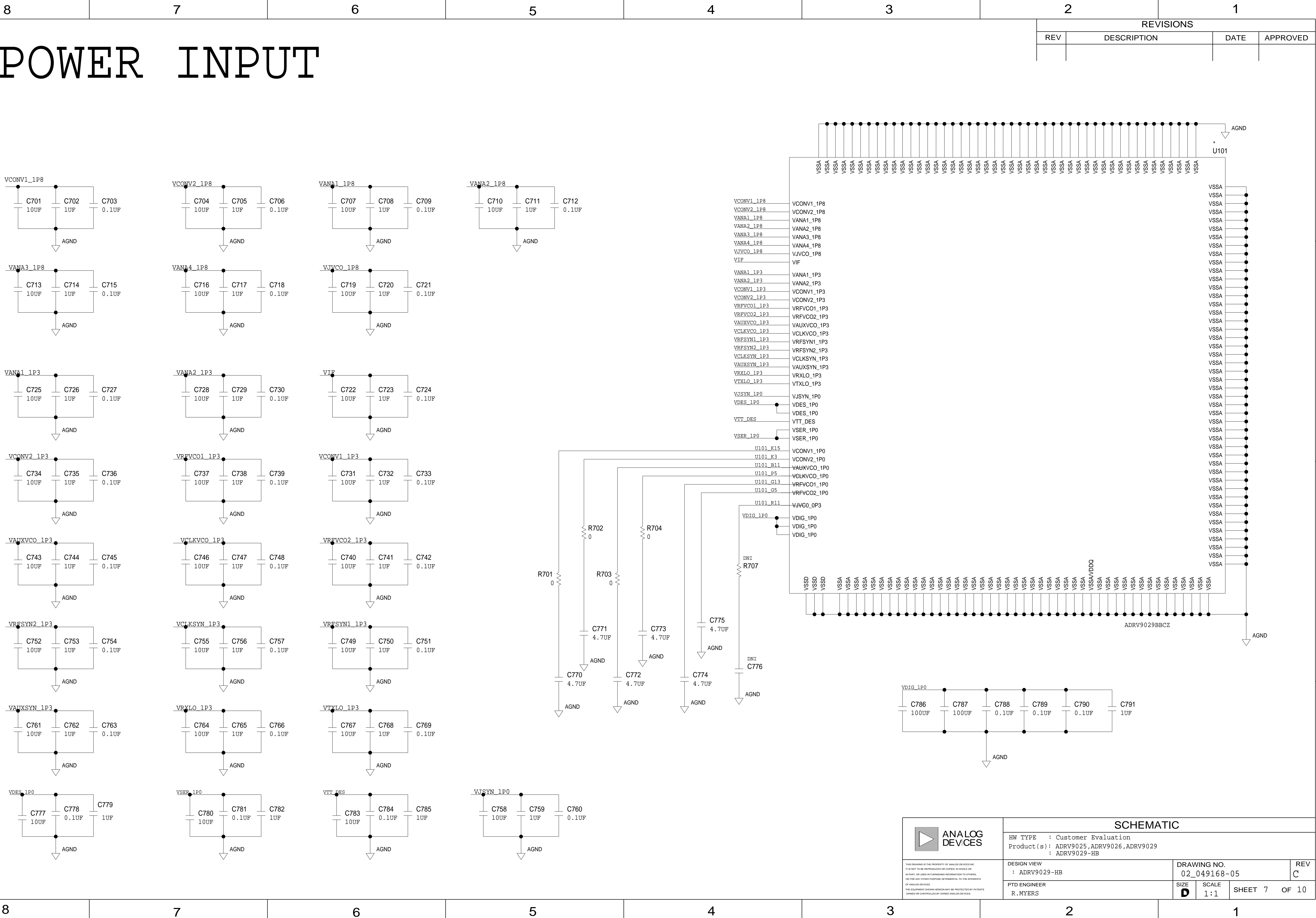
C790

0.1UF

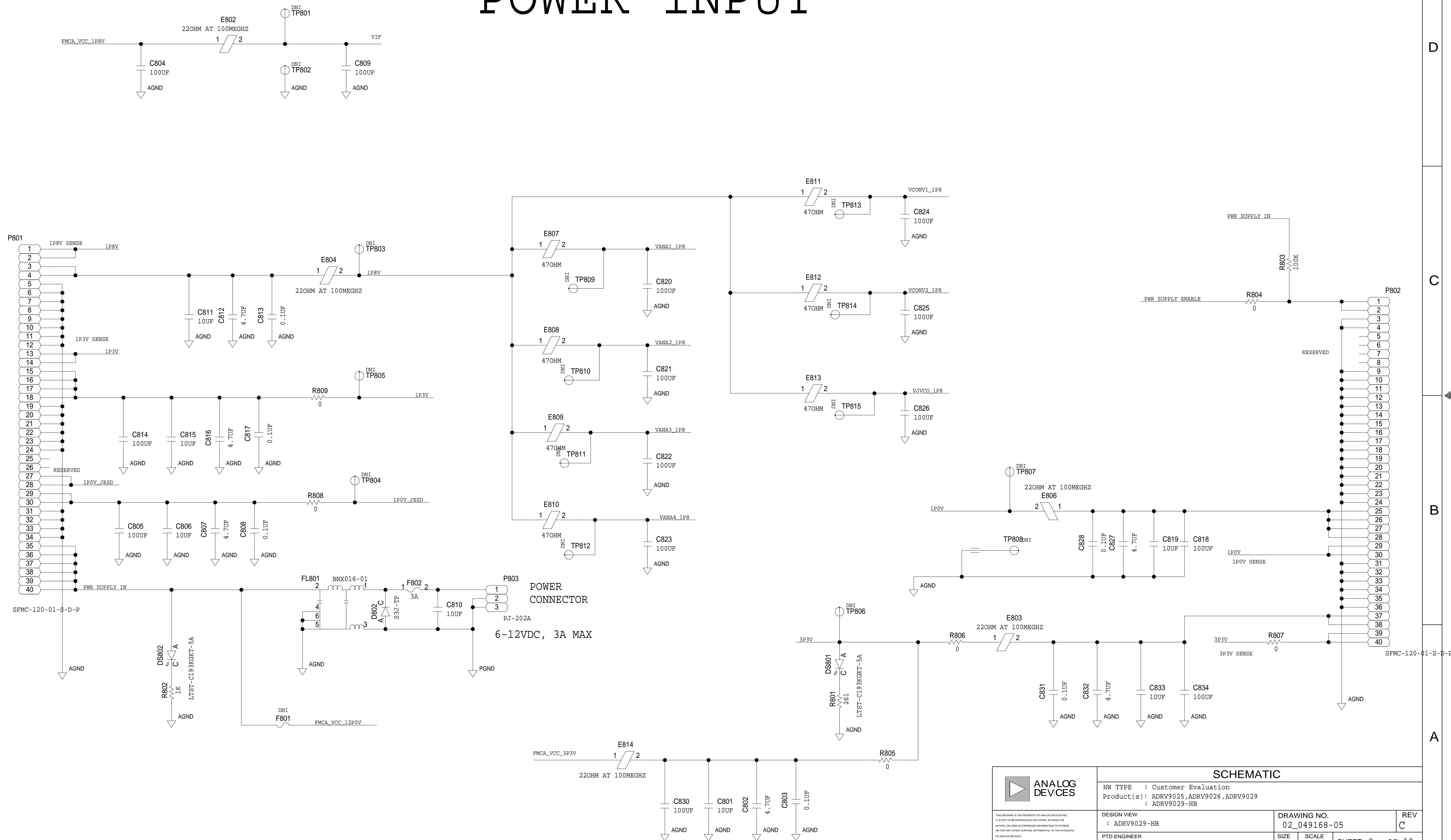
C791

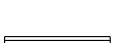
1UF

2
1

[illegible]

POWER INPUT

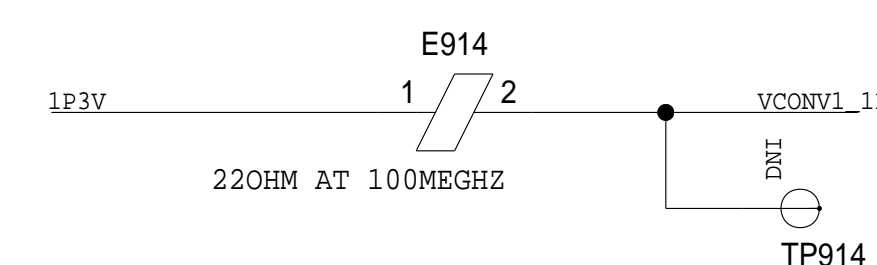
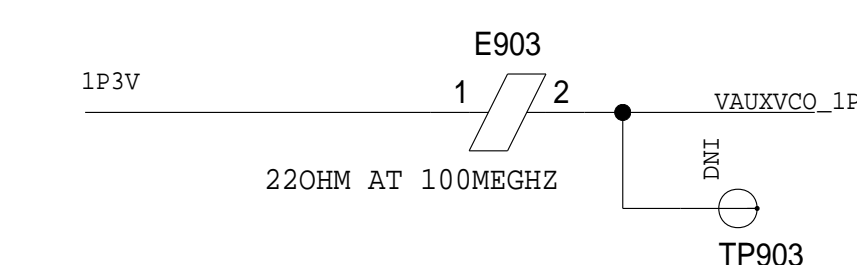
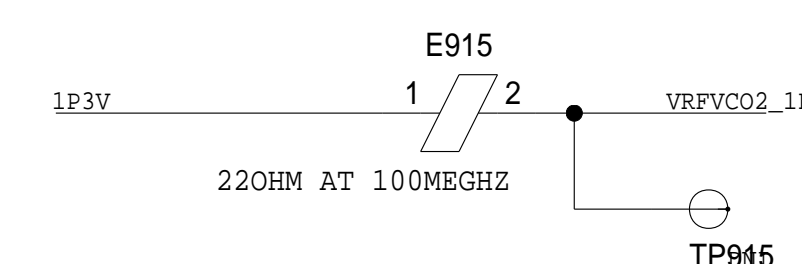
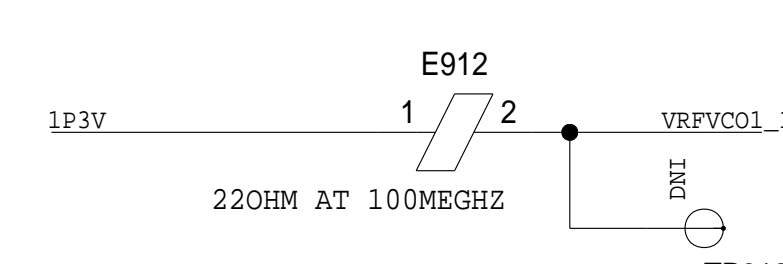
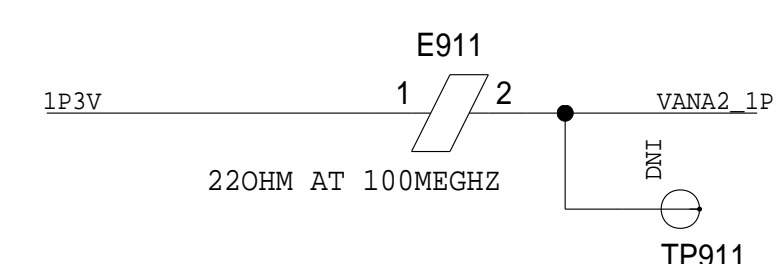
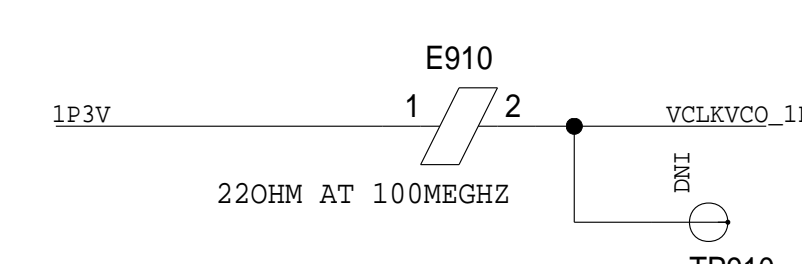
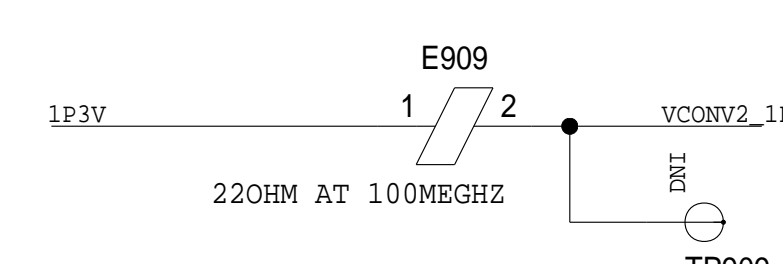
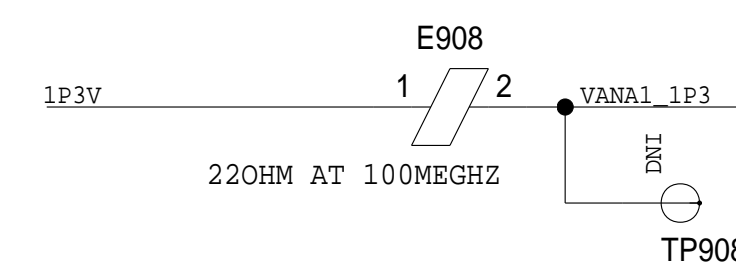
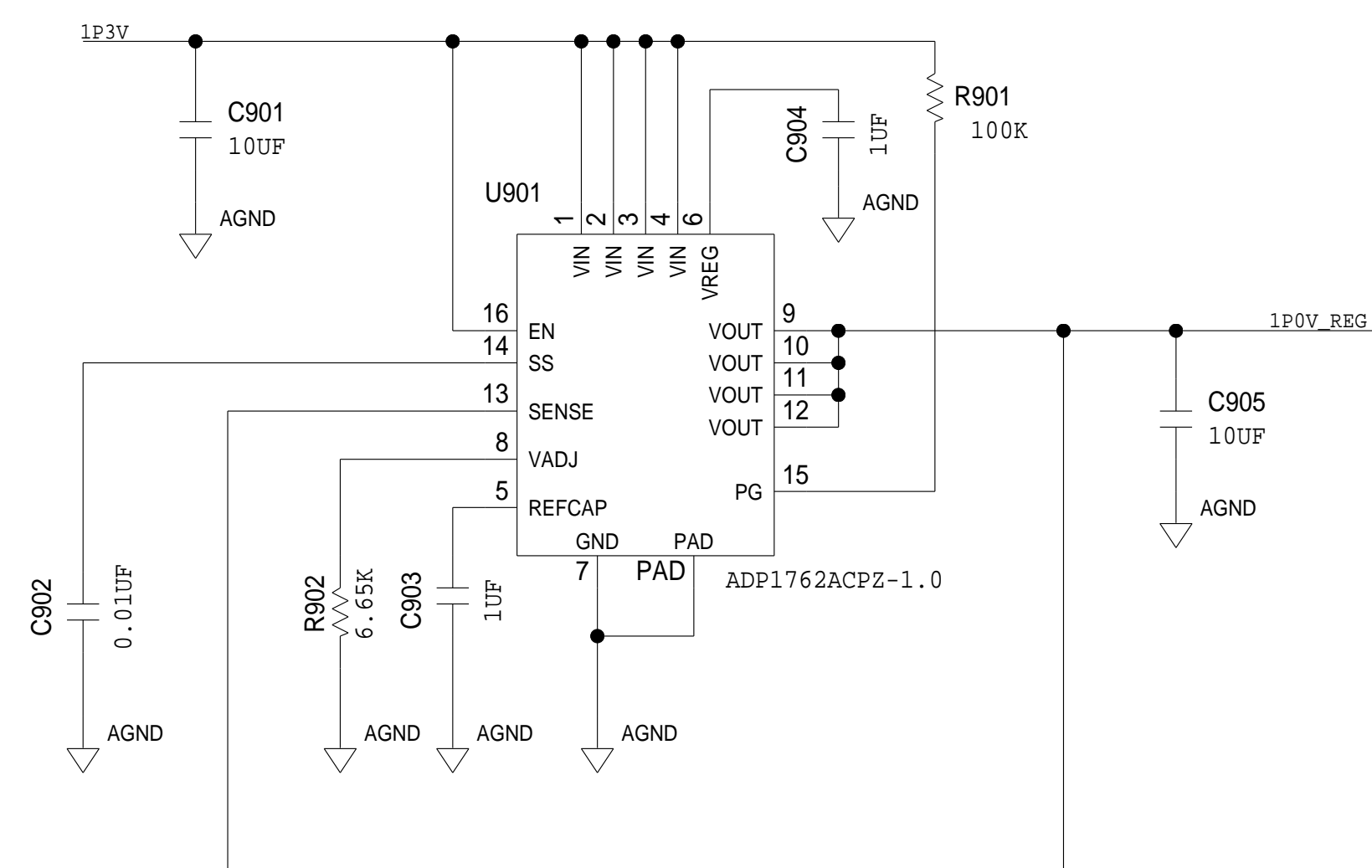


 ANALOG DEVICES	<h1 style="text-align: center;">SCHEMATIC</h1>			
	<p>HW TYPE : Customer Evaluation</p> <p>Product (s) : ADRV9025, ADRV9026, ADRV9029</p> <p style="padding-left: 100px;">: ADRV9029-HB</p>			
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	<p>PTD ENGINEER</p> <p>R.MYERS</p>	<p>SIZE</p> <p>D</p>	<p>SCALE</p> <p>1:1</p>	<p>SHEET 8 OF 10</p>

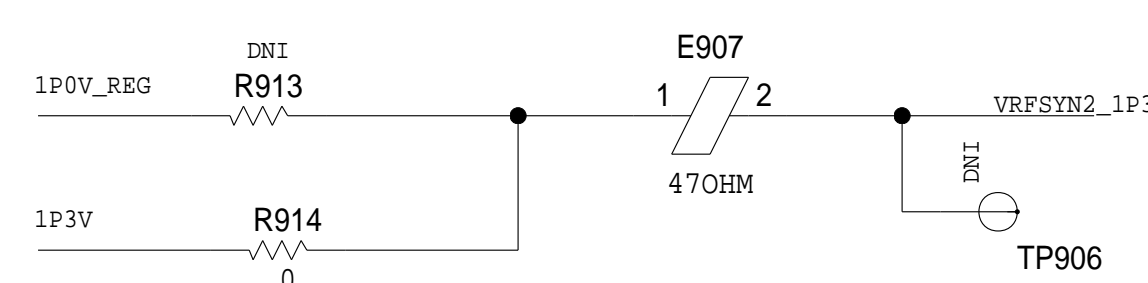
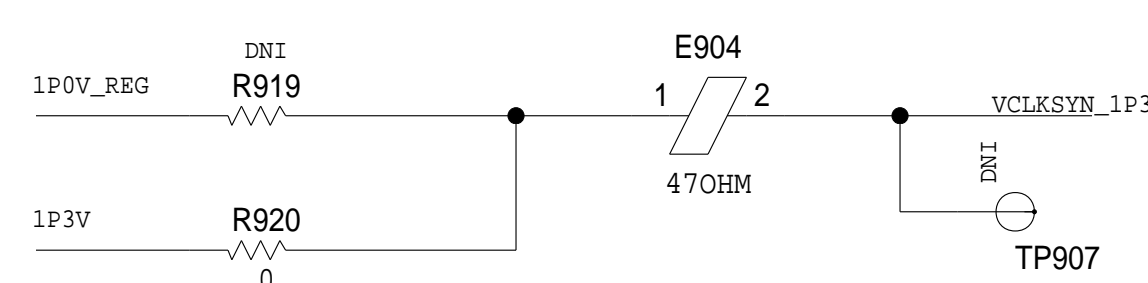
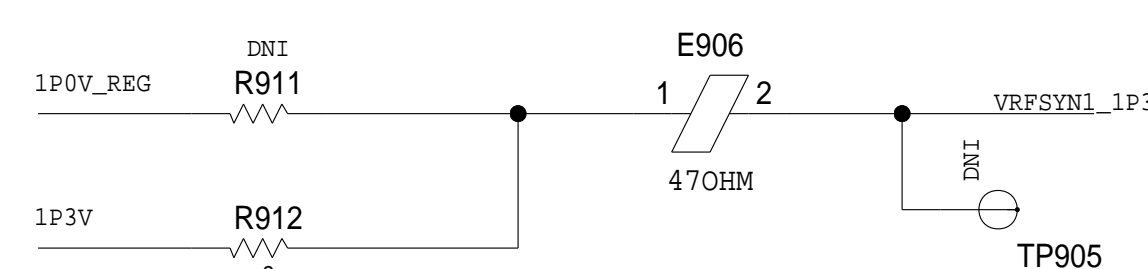
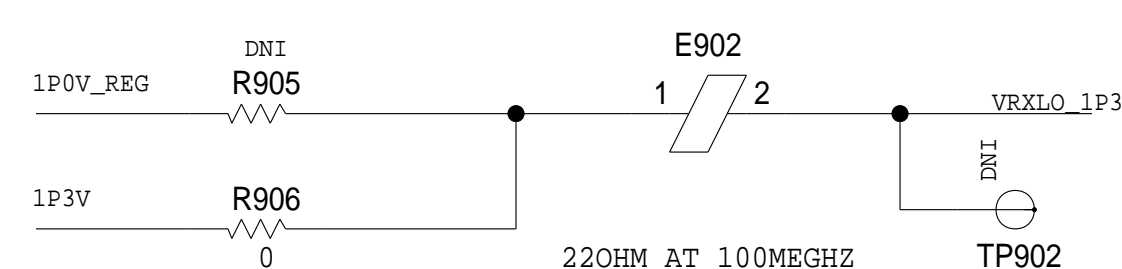
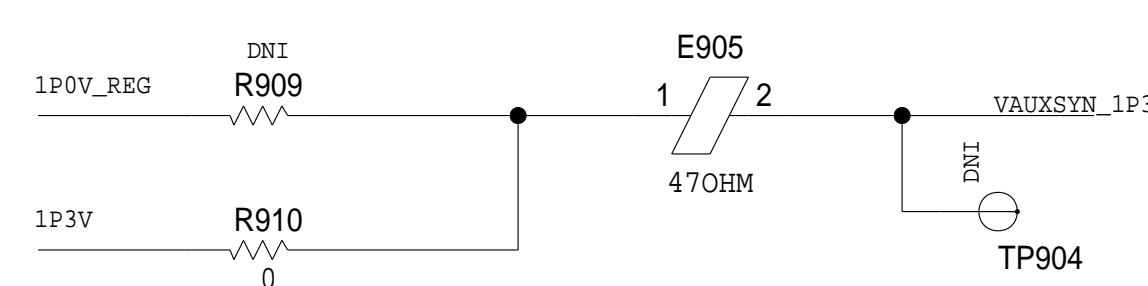
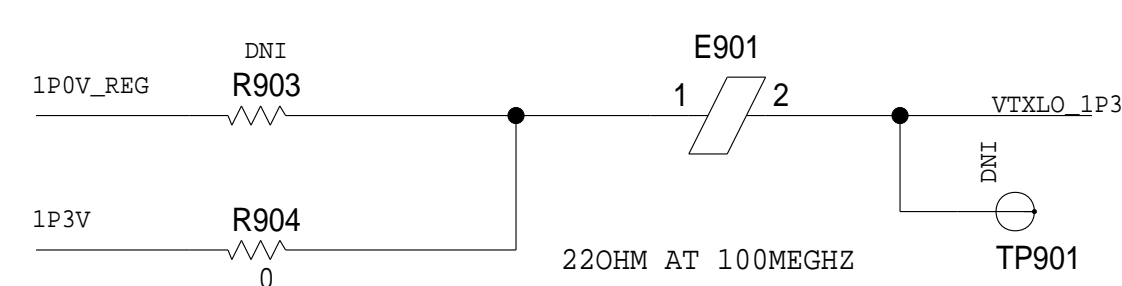
POWER DISTRIBUTION

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

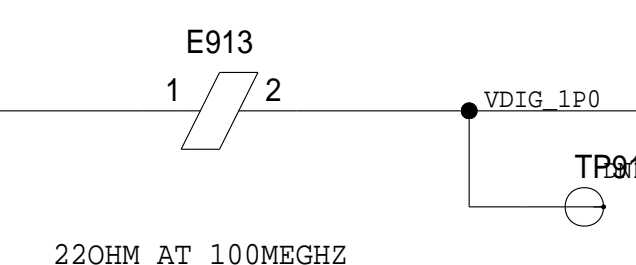
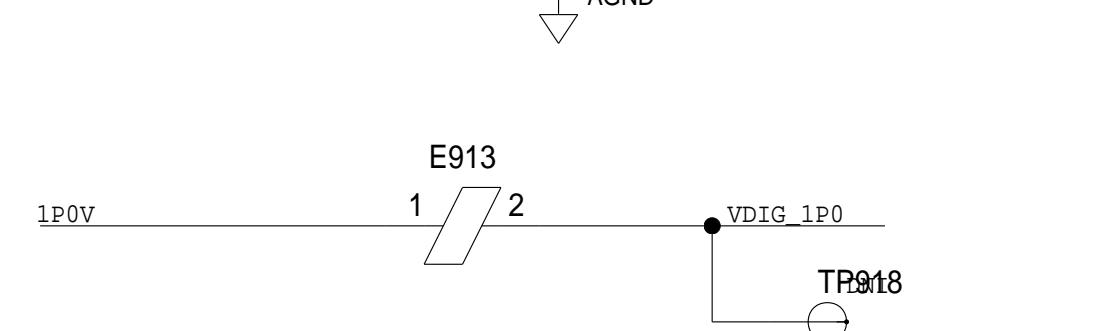
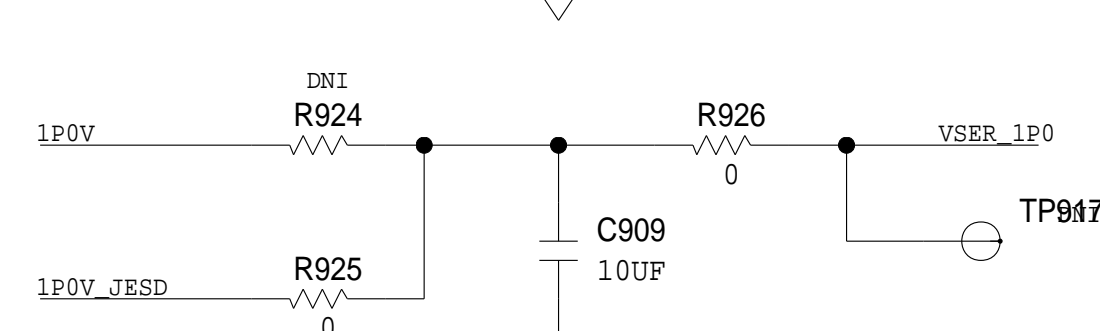
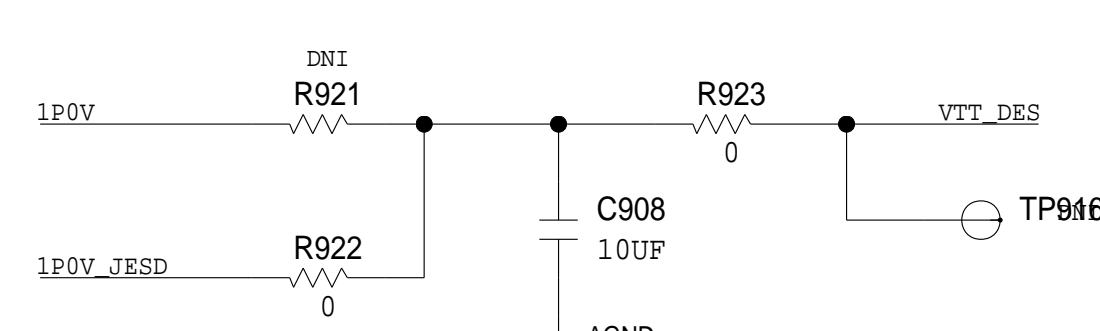
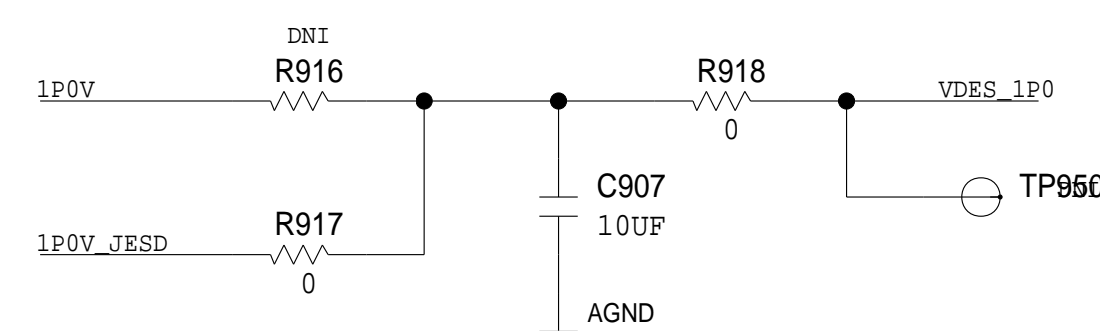
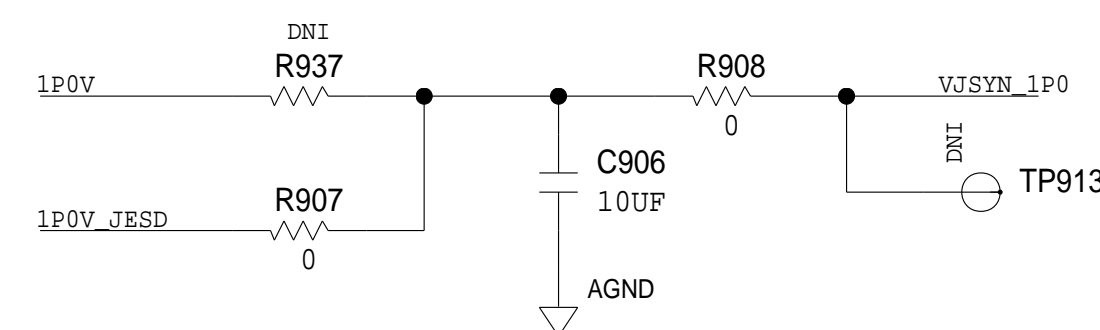
1.3V ANALOG SUPPLIES



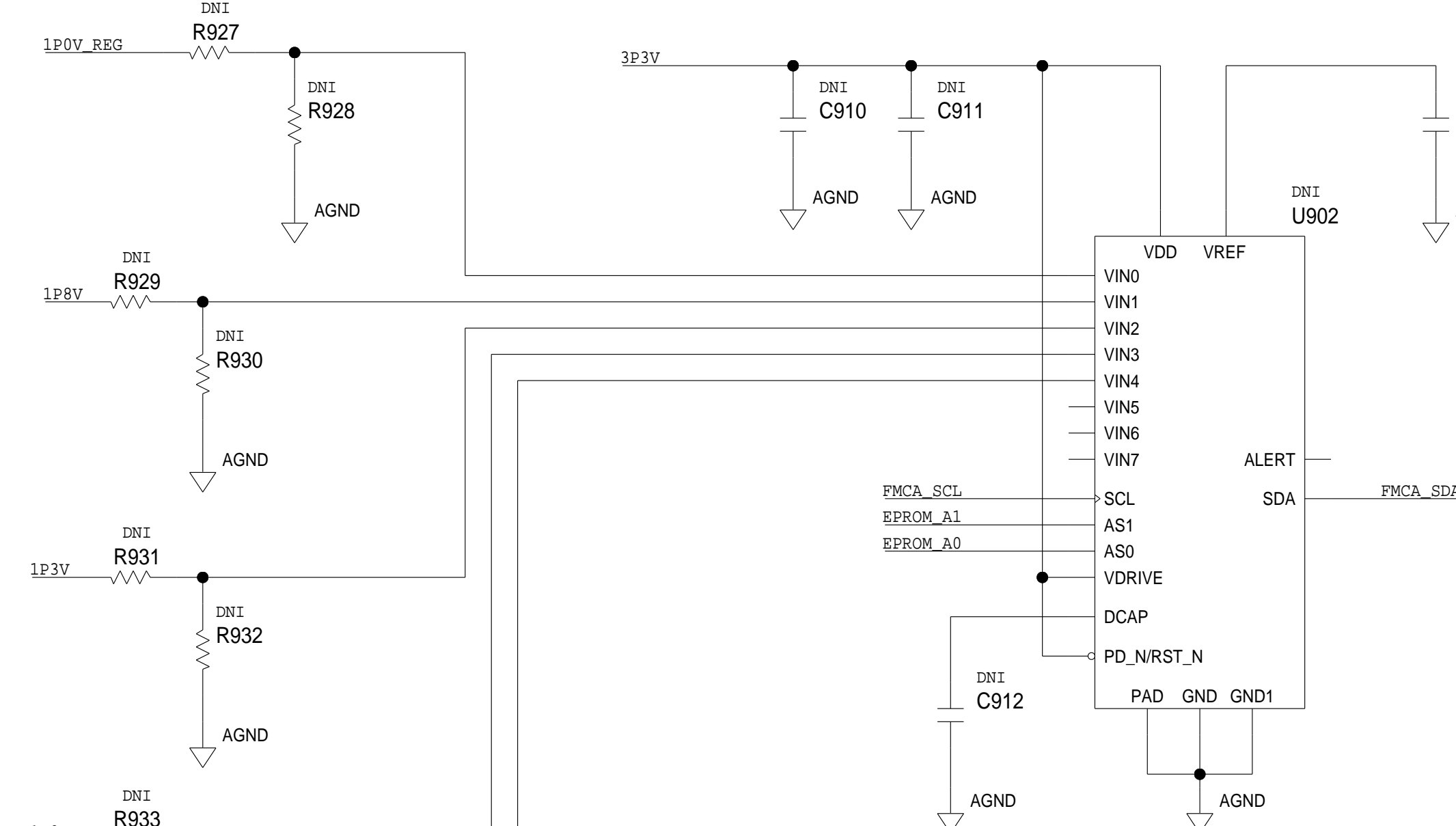
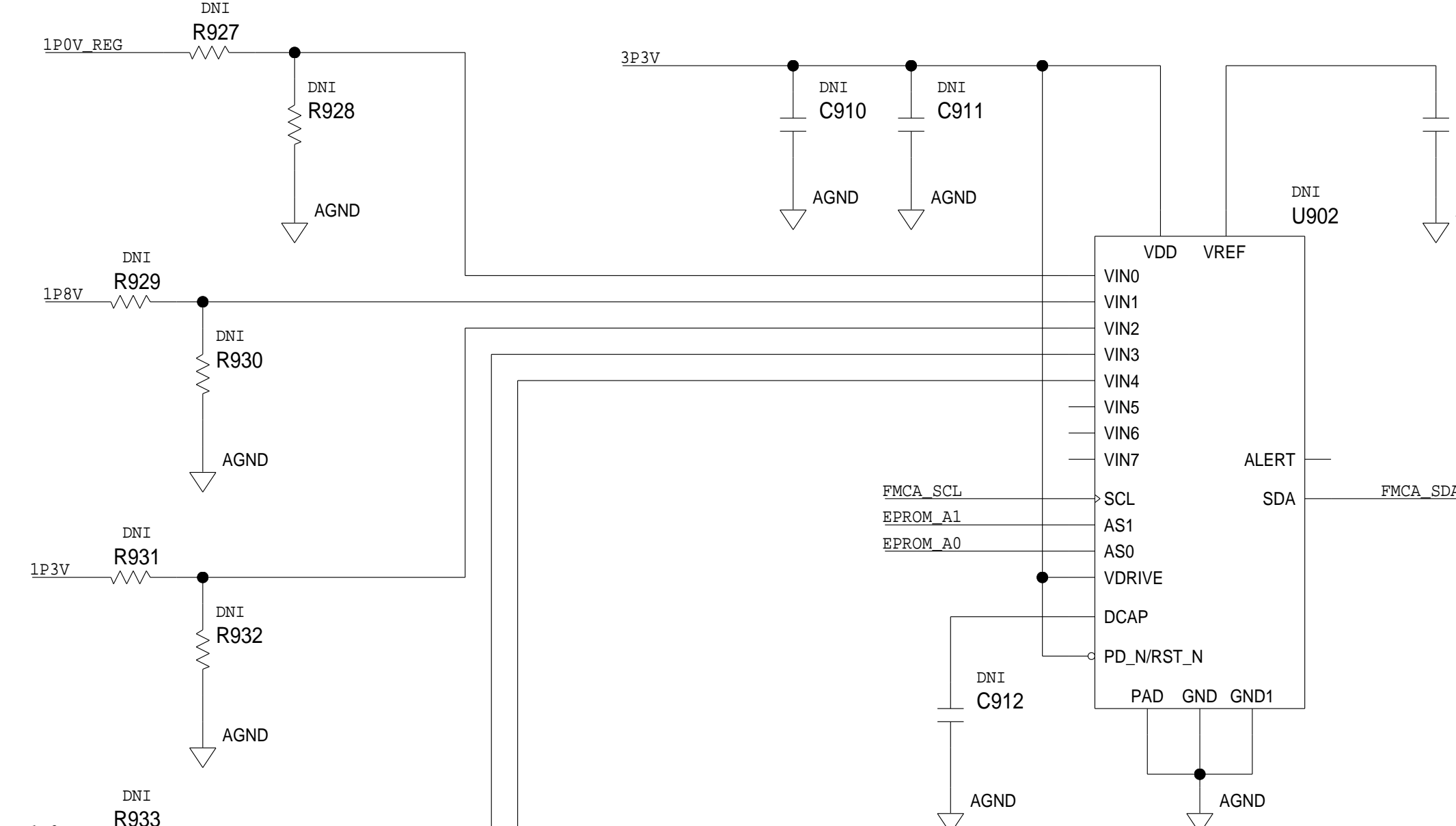
1.0V ANALOG SUPPLIES WITH 1.3V OPTION

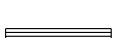


1.0V DIGITAL SUPPLIES



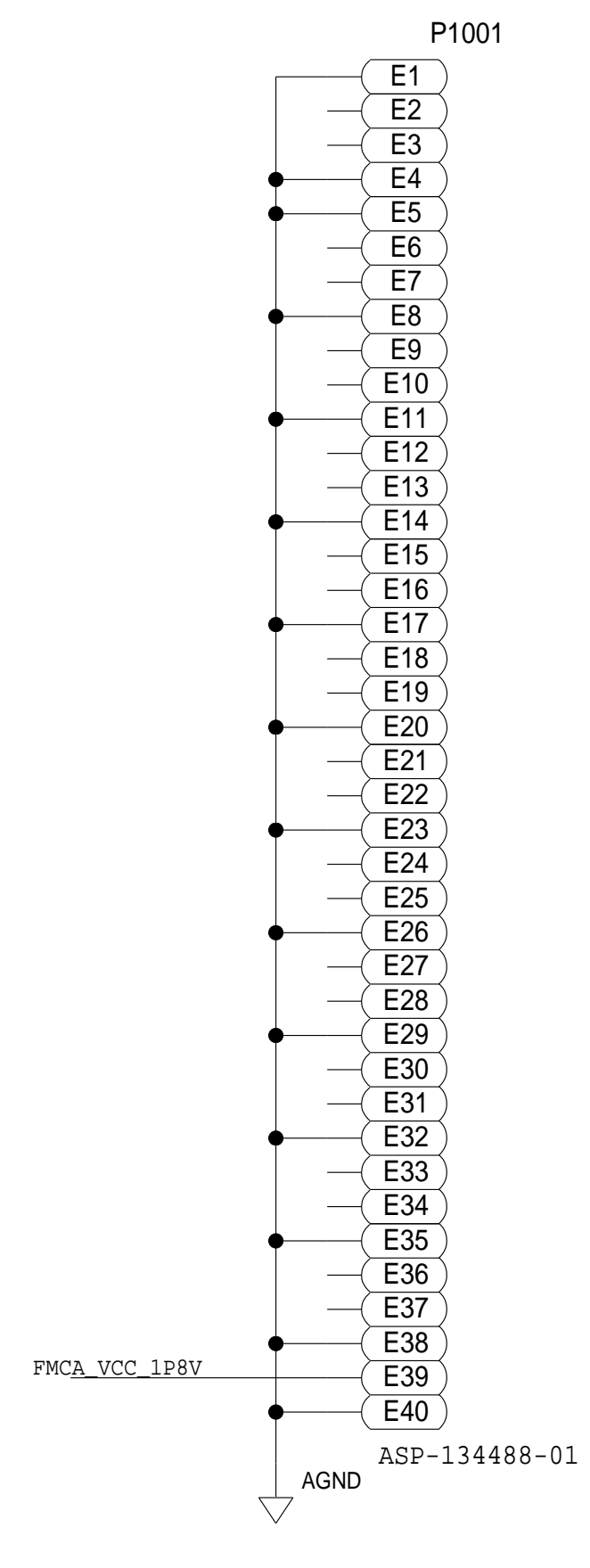
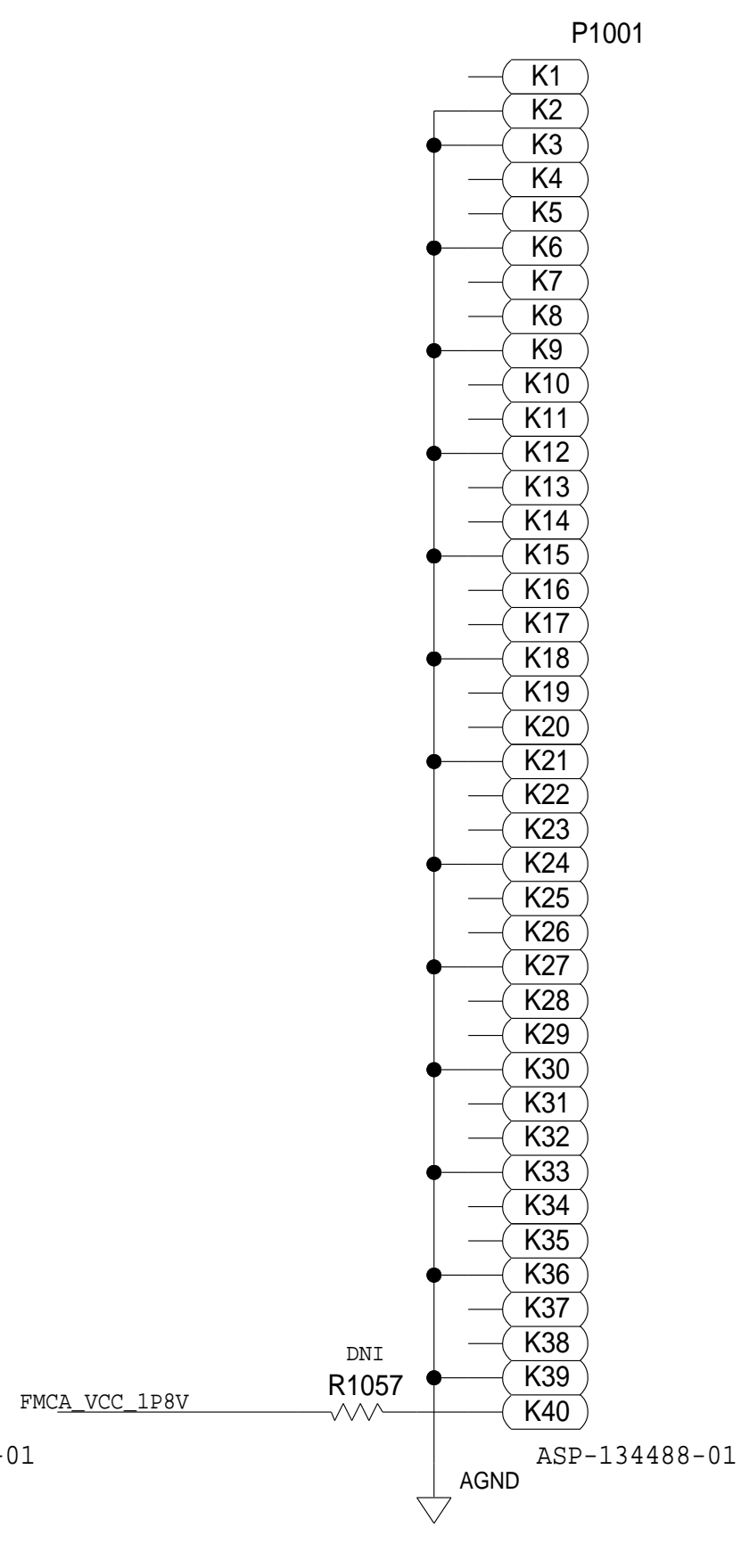
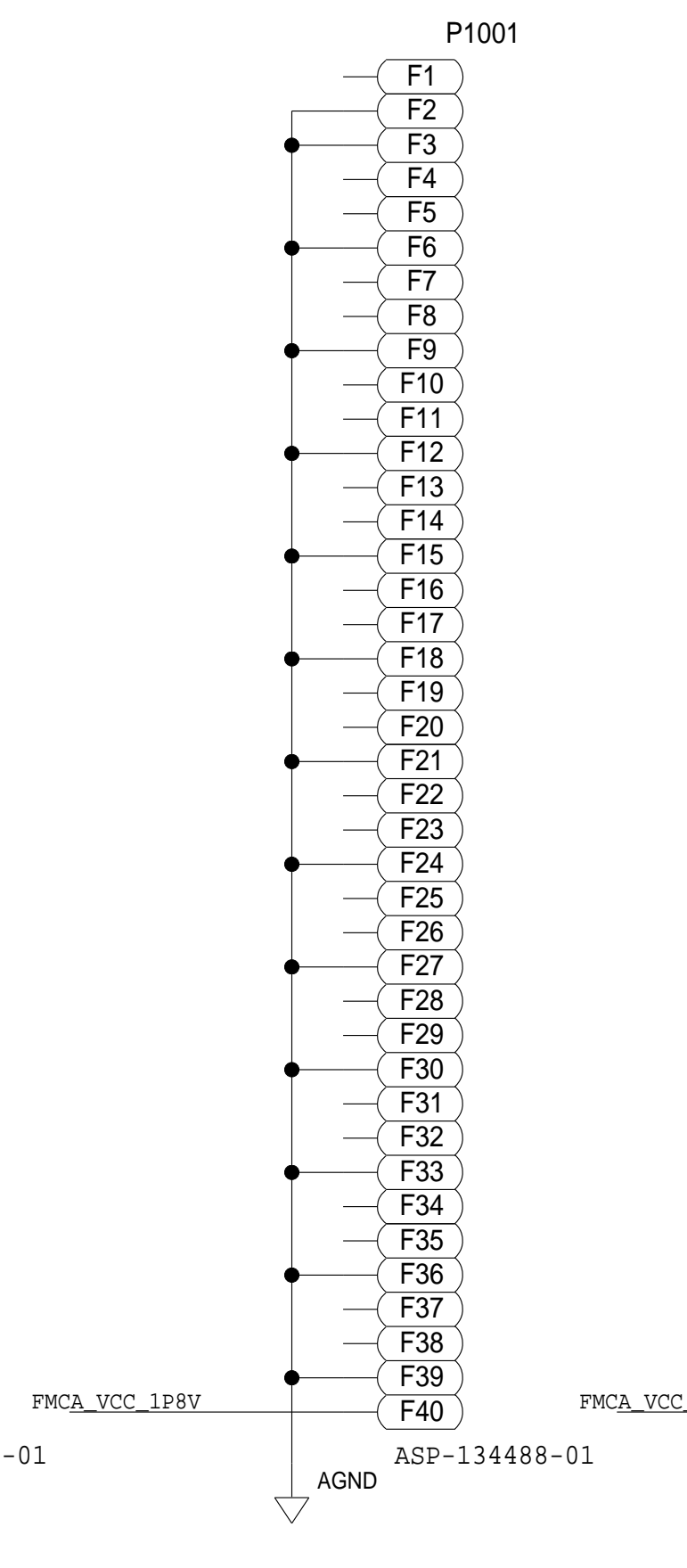
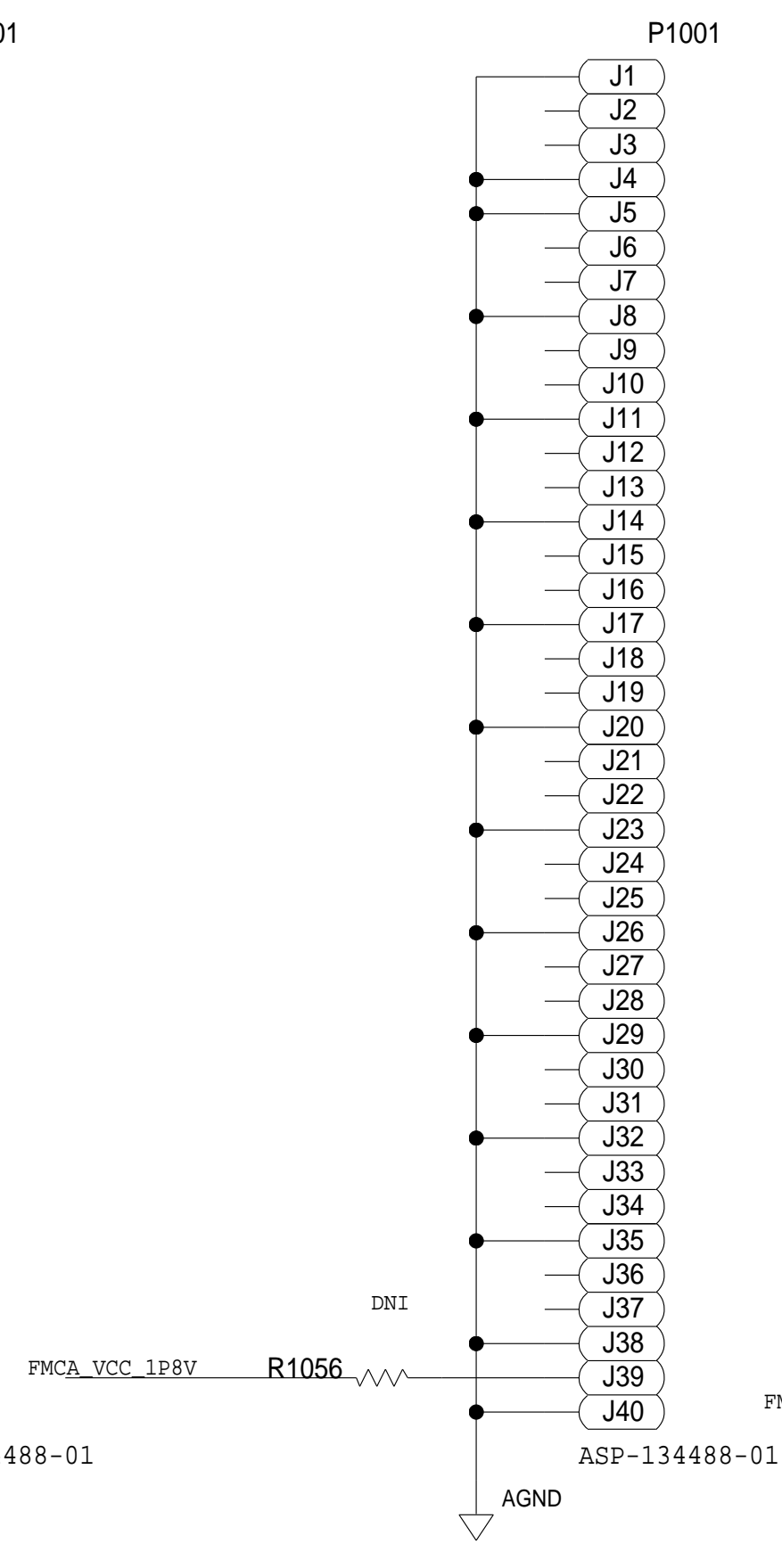
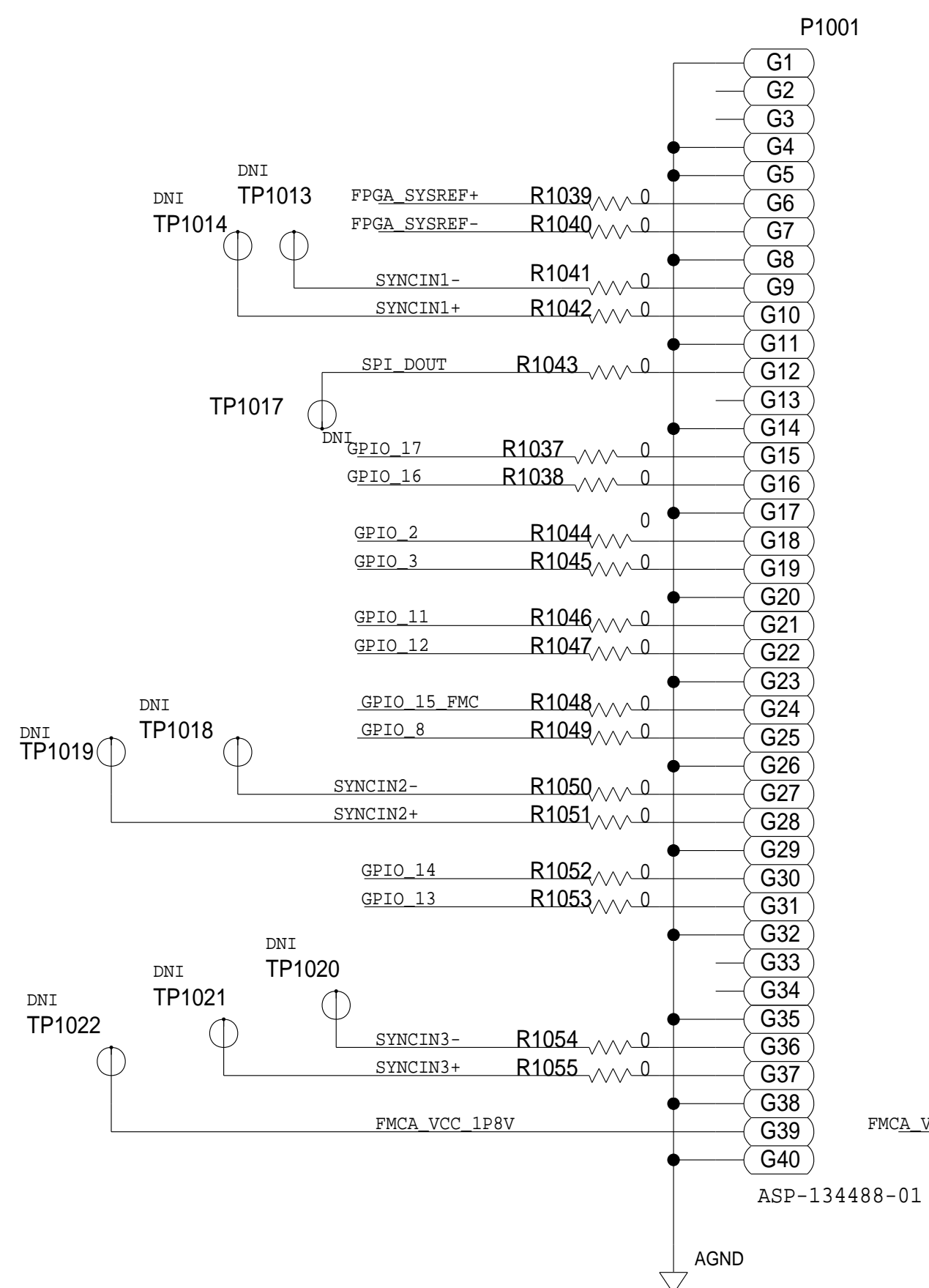
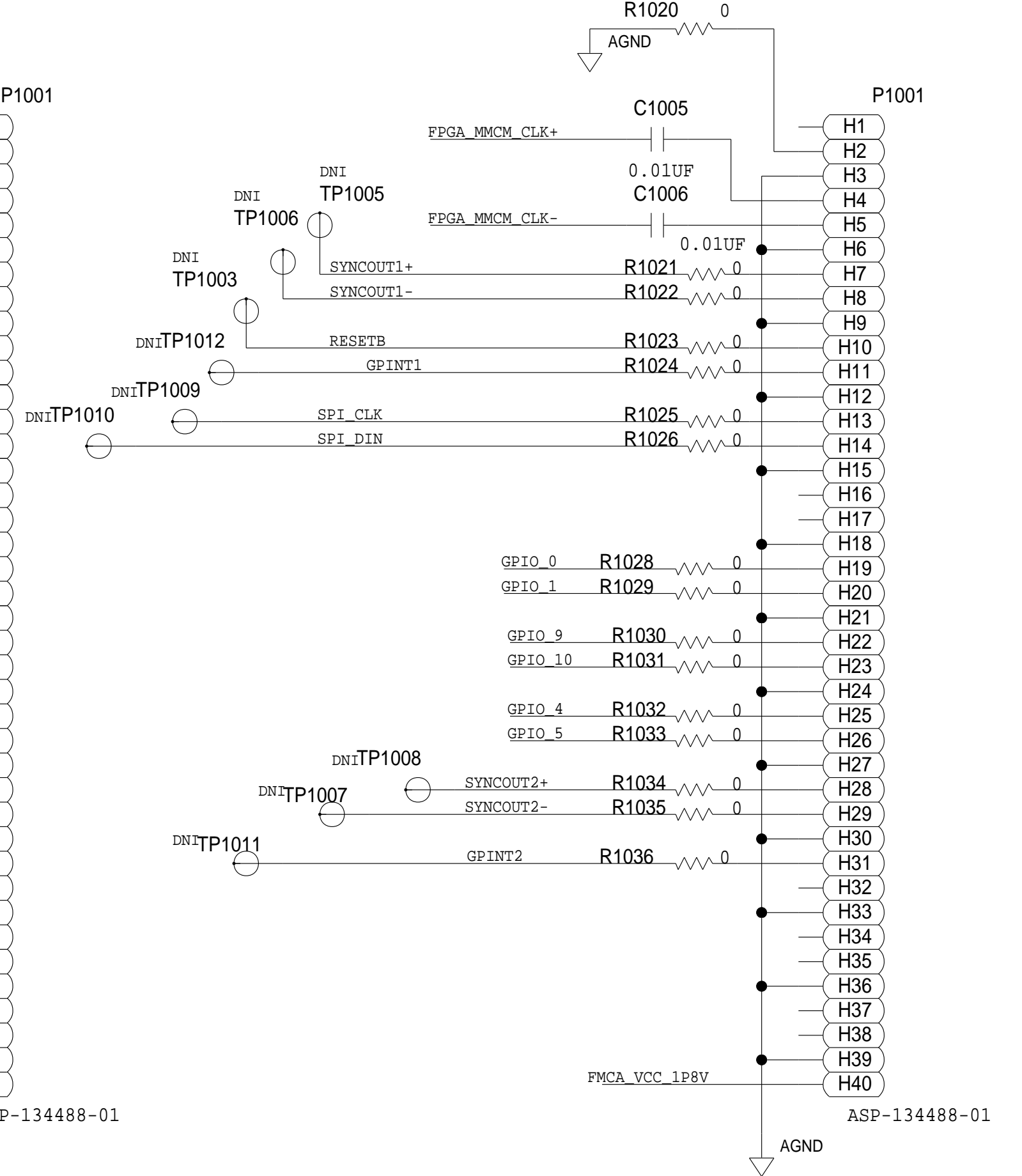
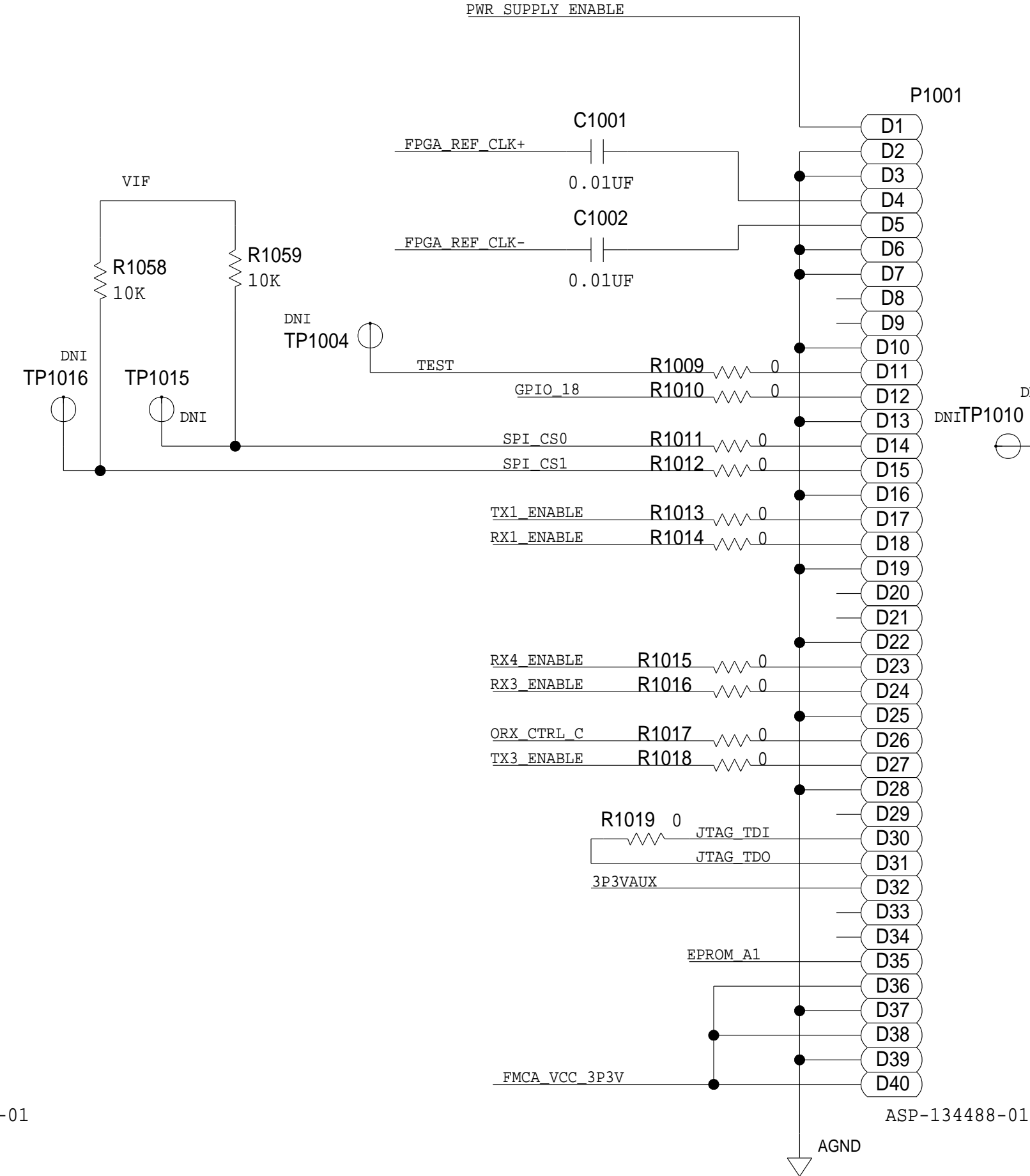
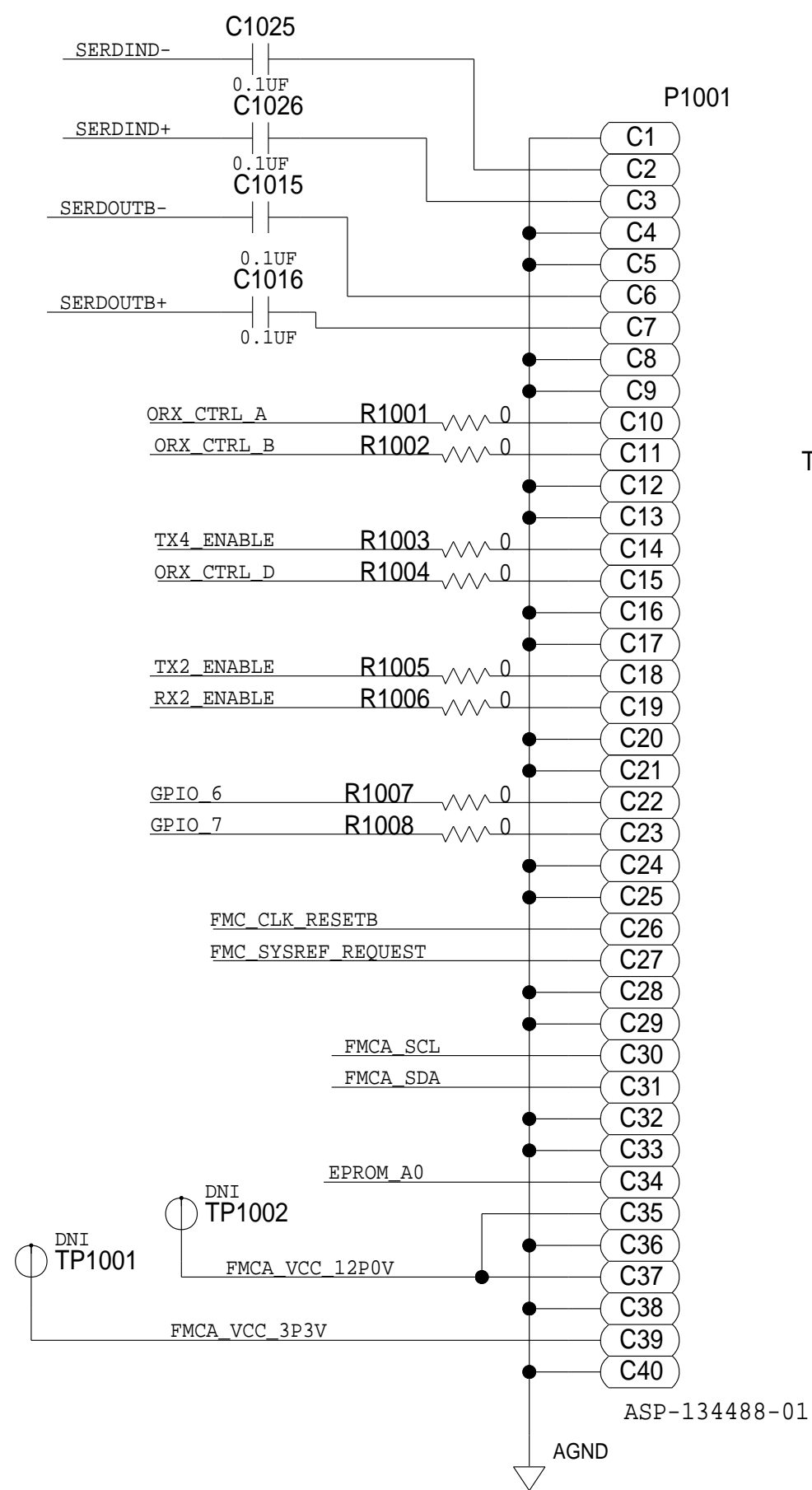
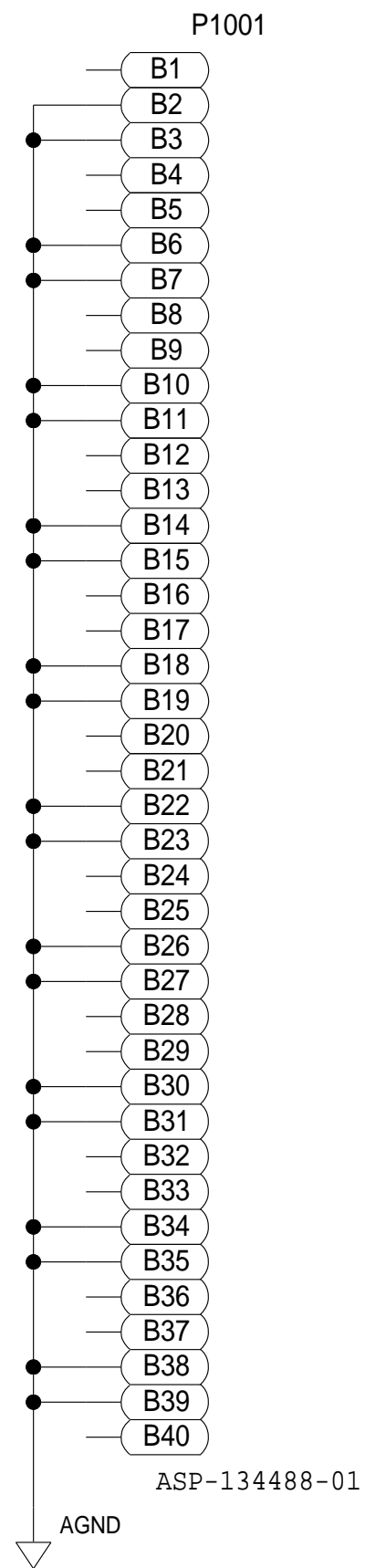
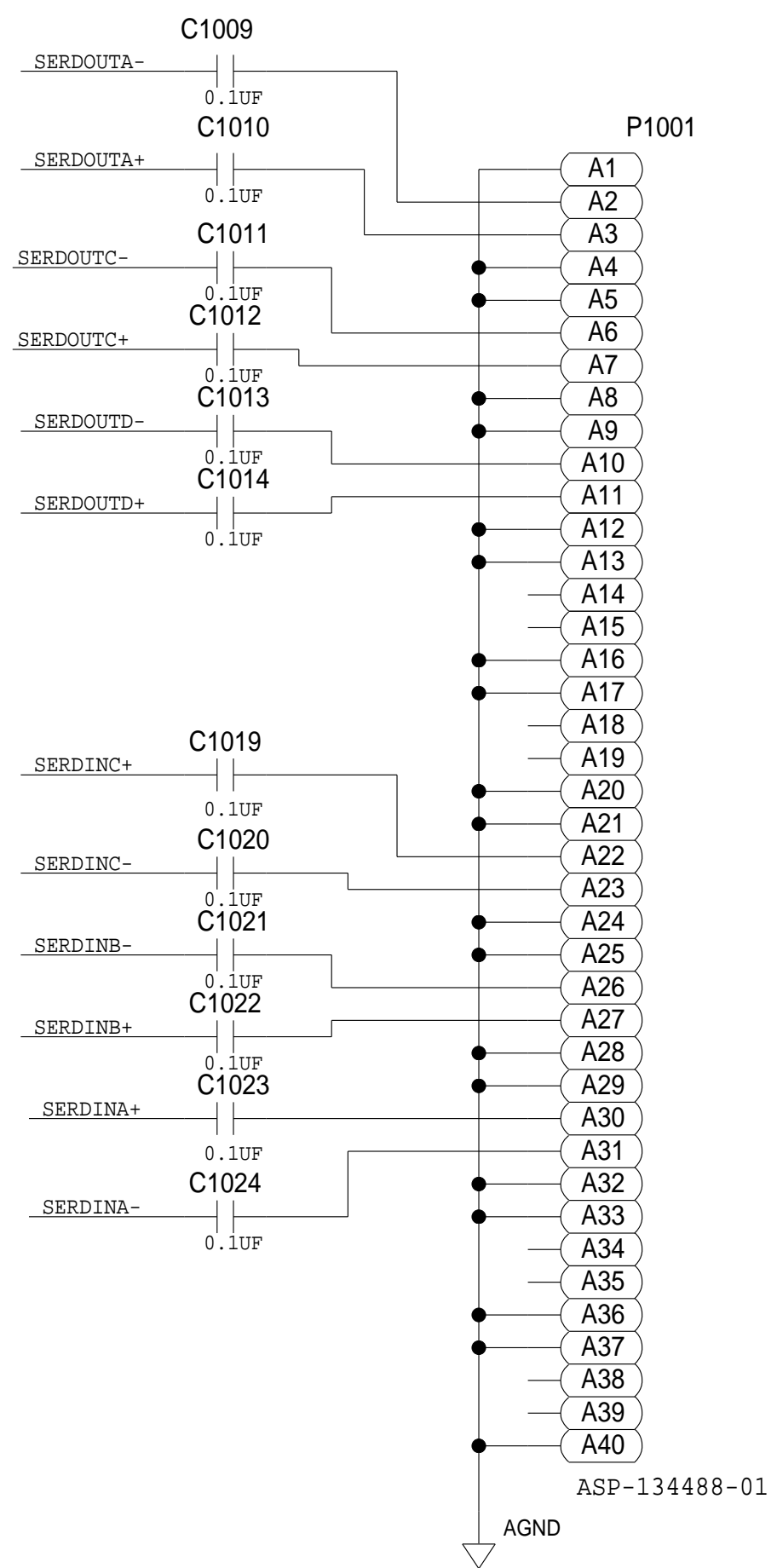
SUPPLY TEST MONITOR



 ANALOG DEVICES	<h1>SCHEMATIC</h1>			
	HW TYPE : Customer Evaluation Product(s) : ADRV9025, ADRV9026, ADRV9029 : ADRV9029-HB			
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	PTD ENGINEER R. MYERS	SIZE D	SCALE 1:1	SHEET 9 OF 10

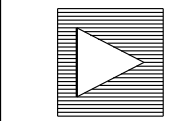
FPGA CONNECTIONS

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



U101			
<u>SERDOUTA+</u>	<u>T7</u>	SERDOUTA+	SERDINA+
<u>SERDOUTA-</u>	<u>T8</u>	SERDOUTA-	<u>SERDINA-</u>
<u>SERDOUTB+</u>	<u>U5</u>	SERDOUTB+	<u>SERDINB-</u>
<u>SERDOUTB-</u>	<u>U6</u>	SERDOUTB-	<u>SERDINB-</u>
<u>SERDOUTC+</u>	<u>T3</u>	SERDOUTC+	<u>SERDINC+</u>
<u>SERDOUTC-</u>	<u>T4</u>	SERDOUTC-	<u>SERDINC-</u>
<u>SERDOUTD+</u>	<u>U1</u>	SERDOUTD+	<u>SERDIND+</u>
<u>SERDOUTD-</u>	<u>U2</u>	SERDOUTD-	<u>SERDIND-</u>
<u>SYNCOUT1+</u>	<u>P15</u>	SYNCOUT1+	<u>SYNCIN1+</u>
<u>SYNCOUT1-</u>	<u>R15</u>	SYNCOUT1-	<u>SYNCIN1-</u>
<u>SYNCOUT2+</u>	<u>N14</u>	SYNCOUT2+	<u>SYNCIN2+</u>
<u>SYNCOUT2-</u>	<u>N15</u>	SYNCOUT2-	<u>SYNCIN2-</u>
		SYNCIN3+	<u>SYNCIN3+</u>
		SYNCIN3-	<u>SYNCIN3-</u>

ADRV9029BBCZ

ANALOG
DEVICES

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SCHEMATIC

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HW TYPE      : Customer Evaluation
Product(s)   : ADRV9025,ADRV9026,ADRV9029
              : ADRV9029-HB
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DESIGN VIEW : ADRV9029-HB
PTD ENGINEER R. MYERS

DRAWING NO.	02_049168-05
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SIZE D	SCALE 1:1	SHEET 10 OF 10
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